

	1	2	3	4	5	6	7	8																																																																												
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B	<div><div>ROMEO DISCRETE DB2 BUILD</div><div>2009/10/21</div></div>																																																																																			
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DATE	CHANGE NO.	REV																																																																																		
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CHANGE by **EDI CHEN** 11-Sep-2009

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Arrandale/  
Clarksfield  
(Socket-rPGA989)

DDR3 SO-DIMM 1  
800/1066/1333 MHz , 8GB maximum memory P20

DDR3 SO-DIMM 2  
800/1066/1333 MHz , 8GB maximum memory P21

PCH  
HM-57

USB 0  
ESATA+USB  
P33

USB 1  
Card Reader  
P44

USB 2  
WLAN+BT  
P42/P60

USB 3  
WWAN  
P60

USB 4  
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P42

USB 5  
Webcam  
P34

USB 8  
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P39

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P06

System Charger  
DC / DC System Power  
P05

SPI ROM  
EC FW  
(BIOS)  
P31

SPI ROM  
BIOS  
P24

Accelerometer  
ST Micro HP302DLTR8-MBD  
P42

IDT 92HD80  
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P42

ITE IT8502E  
KBC  
P31

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P32

P55 SPKR

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SMSC EMC1402  
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SATA  
SATA

SMBUS

HDA

LPC

SPI

P22~30

HSPI

PEG

FDI

DMI

LVDS

PCIE

PCIE

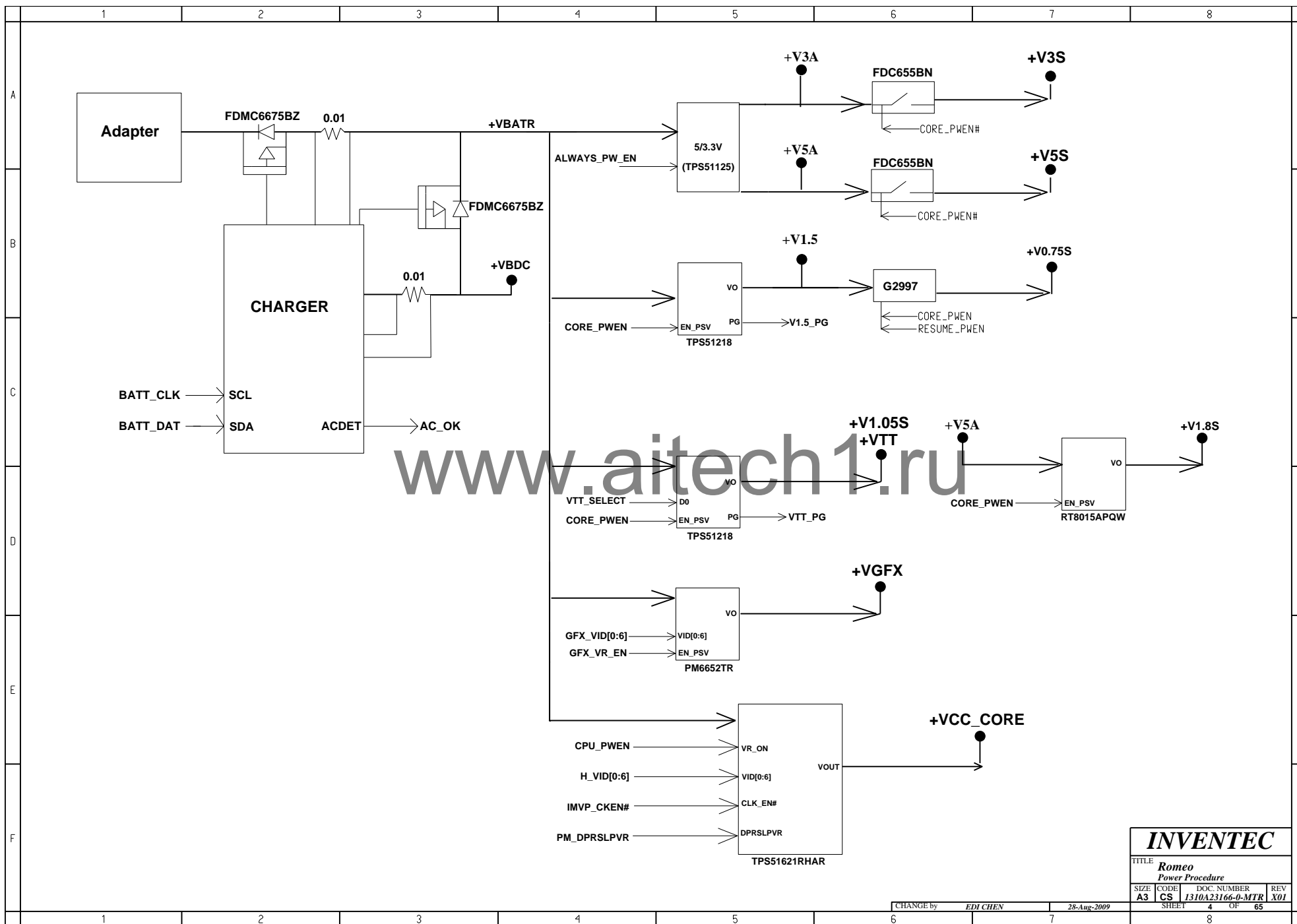
USB

USB

DDR3

DDR3

INVENTEC			
TITLE Romeo Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310A23166-0-MTR	REV X01
CHANGE by EDI CHEN		28-Aug-2009	
SHEET 3		OF 65	



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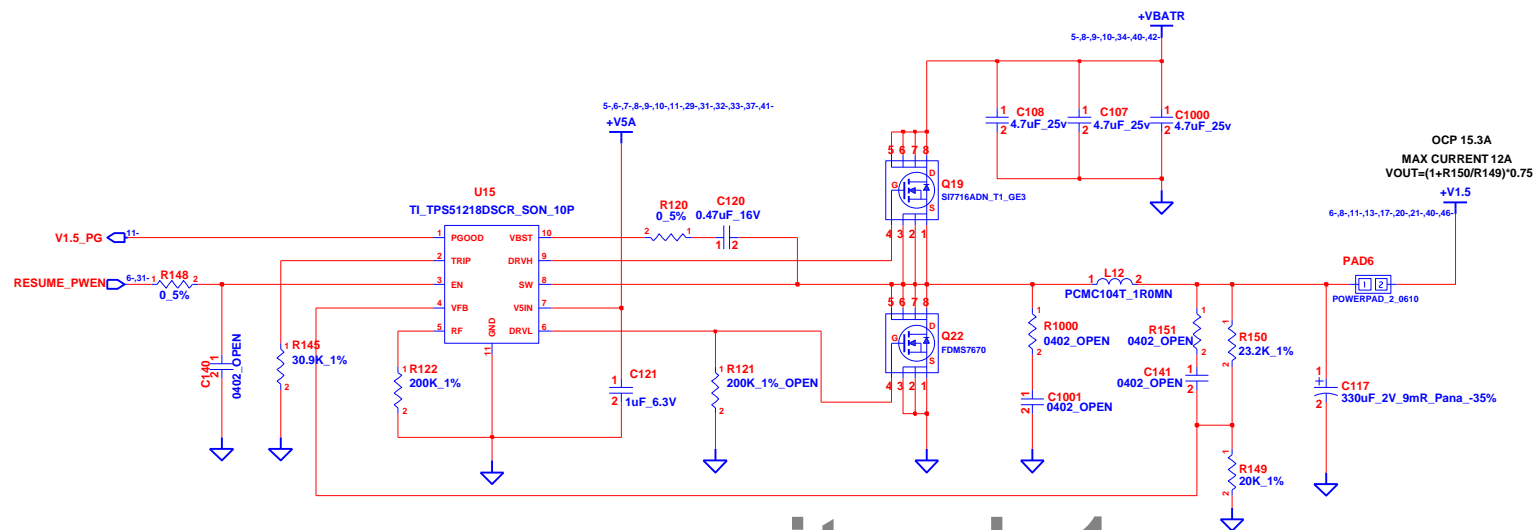
**INVENTEC**

TITLE			
Romeo			
Power Procedure			
SIZE	CODE	DOC. NUMBER	REV
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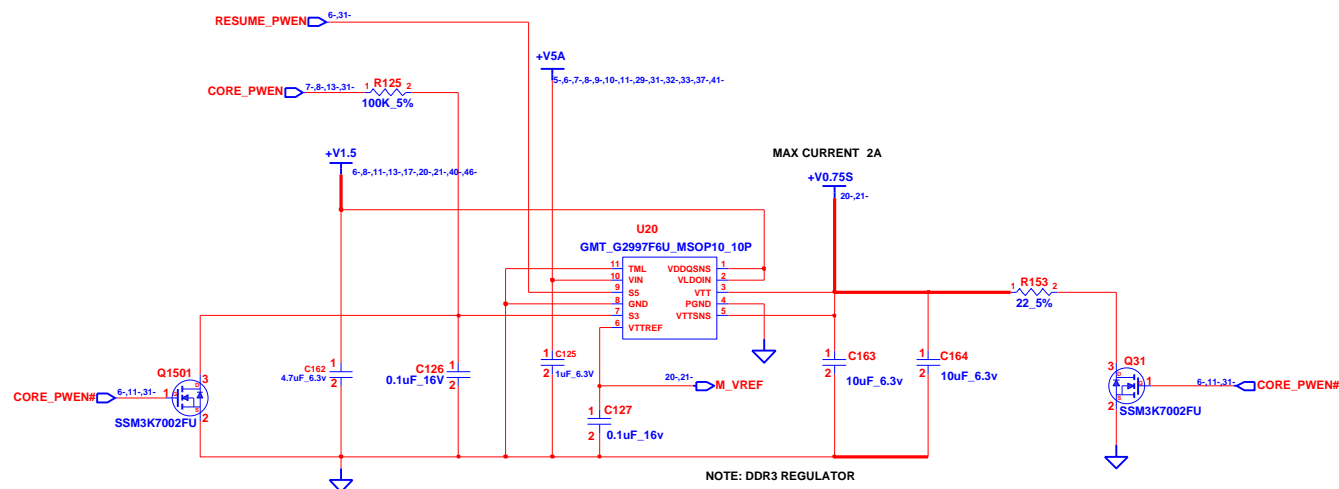
CHANGE by **EDI CHEN** 28-Aug-2009







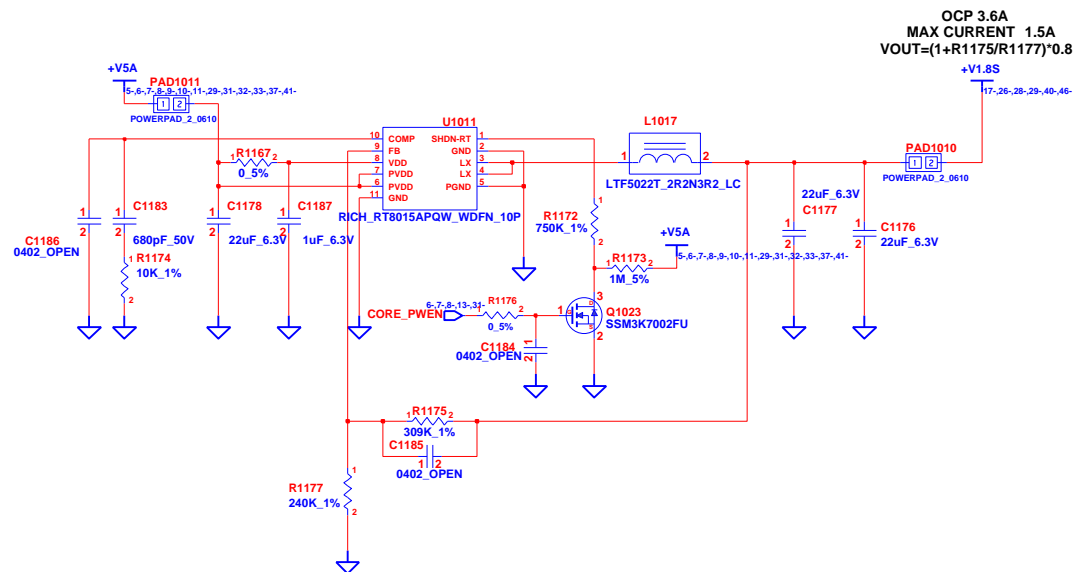
www.aitech1.ru



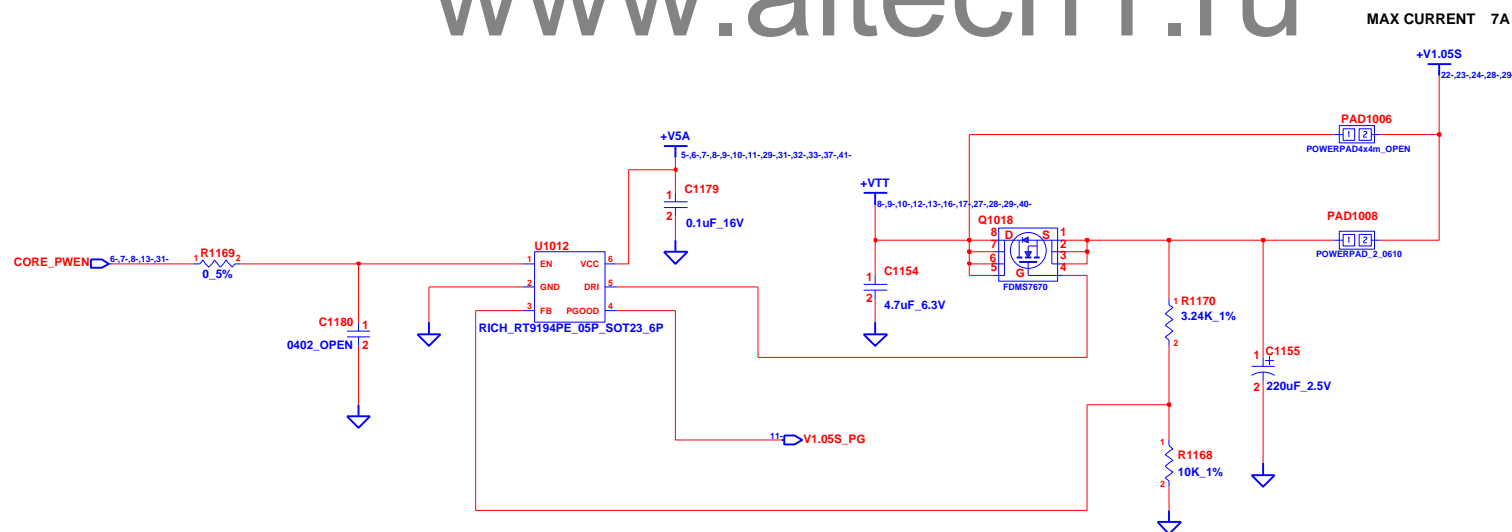
**INVENTEC**

TITLE			
Romeo			
+V1.5 & +V0.75			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		6	OF 65

CHANGE by EDI CHEN 28-Aug-2009



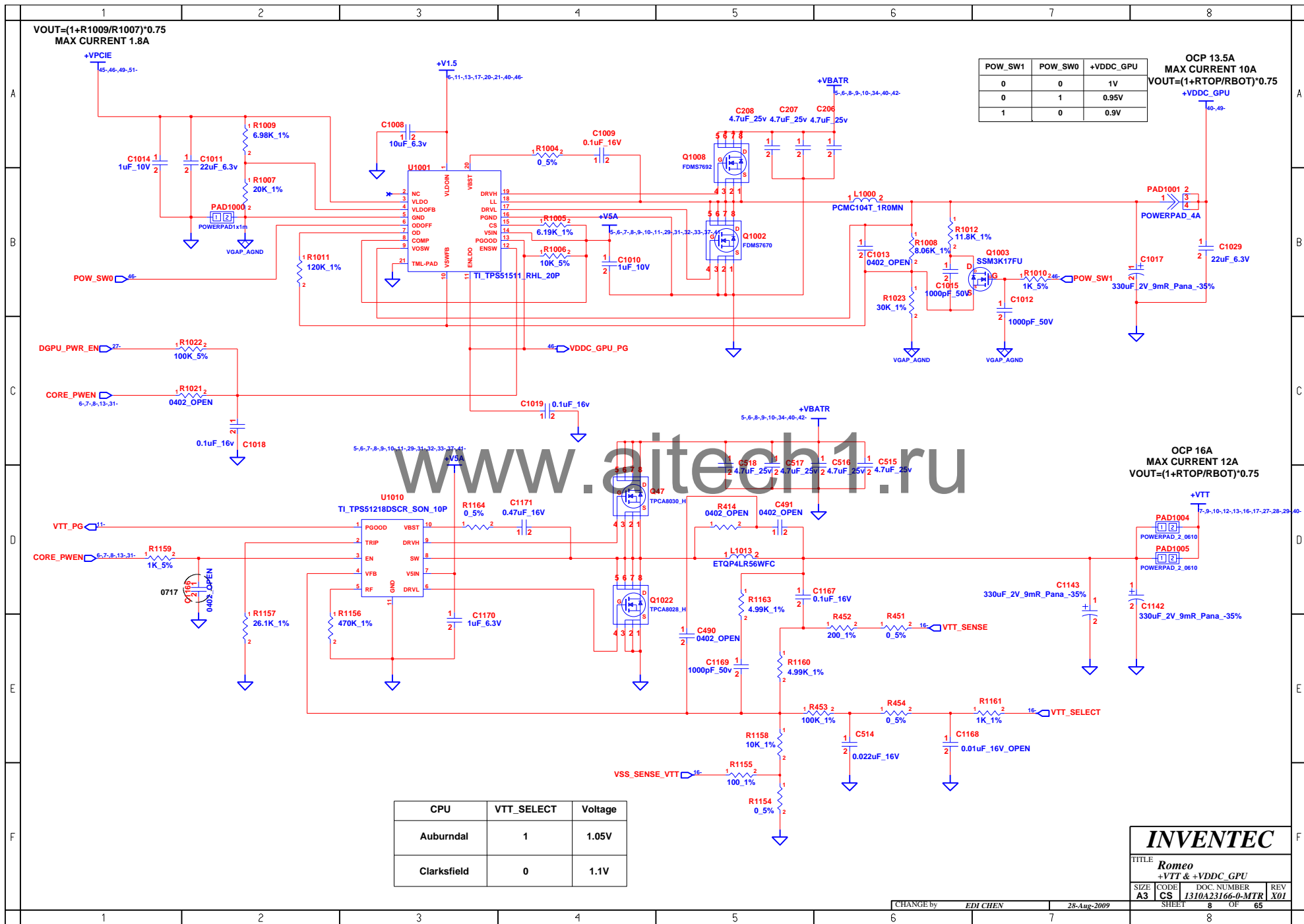
www.aitech1.ru

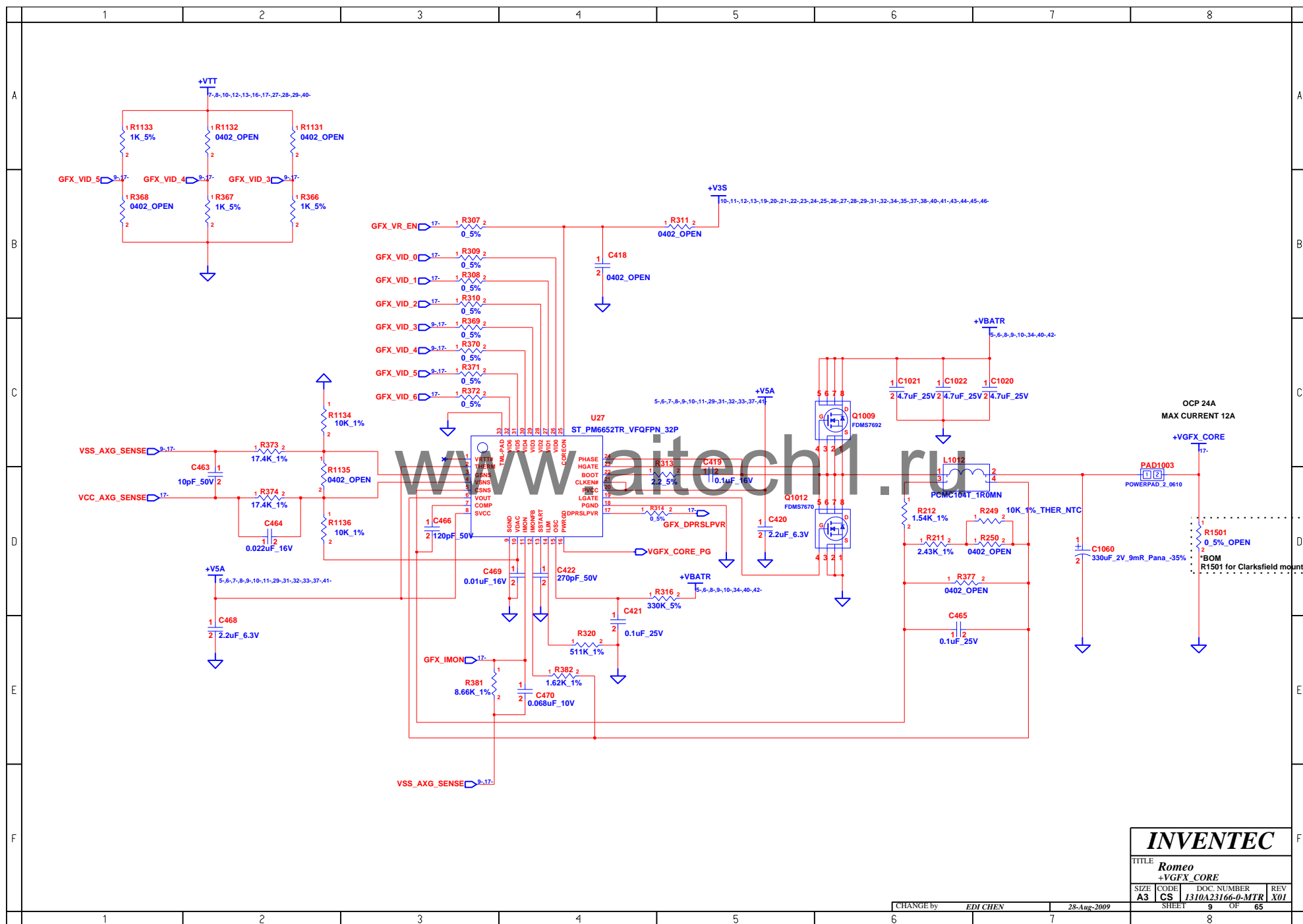


**INVENTEC**

TITLE			
Romeo			
+V1.8S & +V1.05S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		OF	65
		7	8

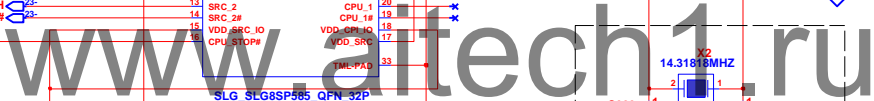
CHANGE by EDI CHEN 28-Aug-2009





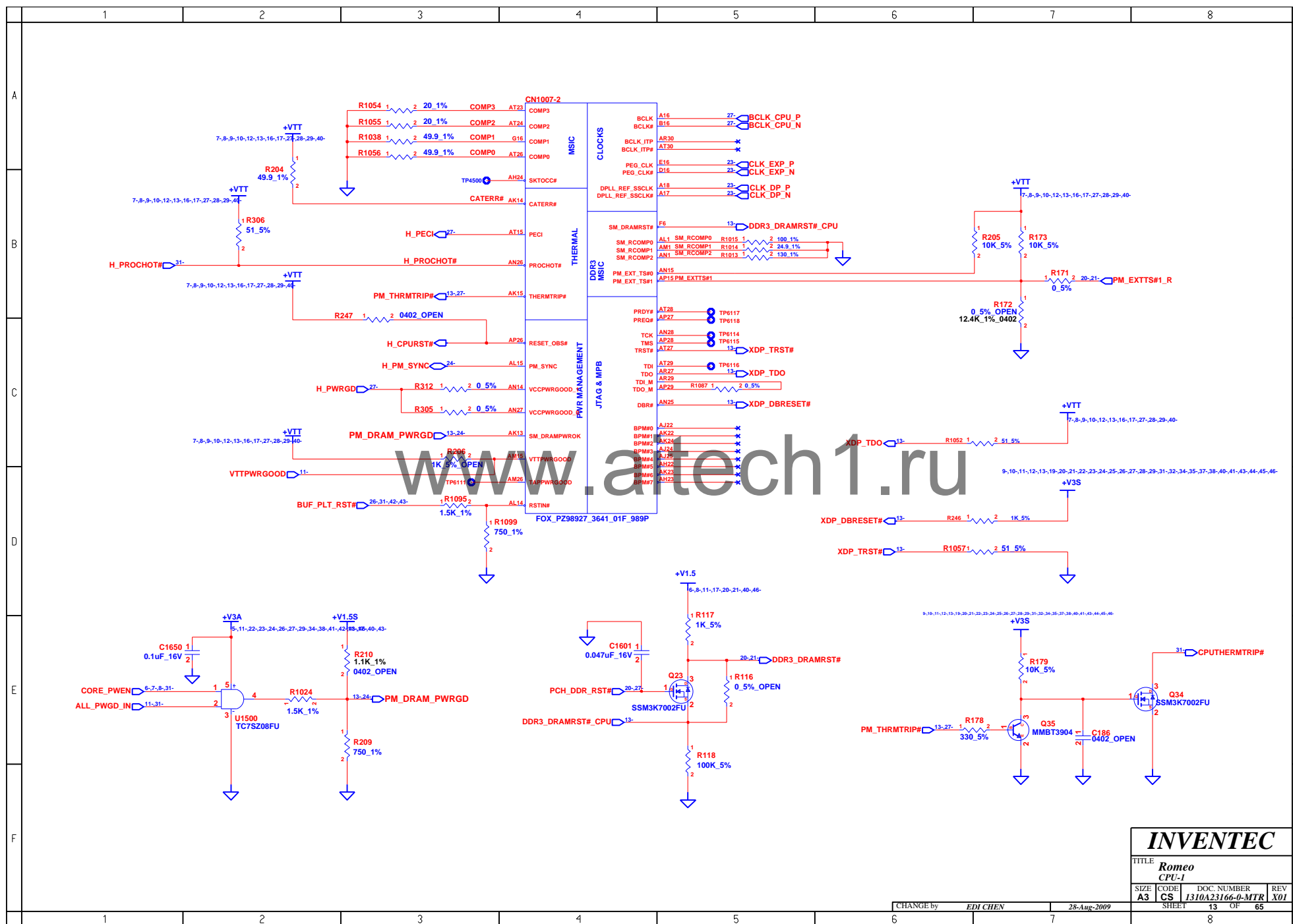


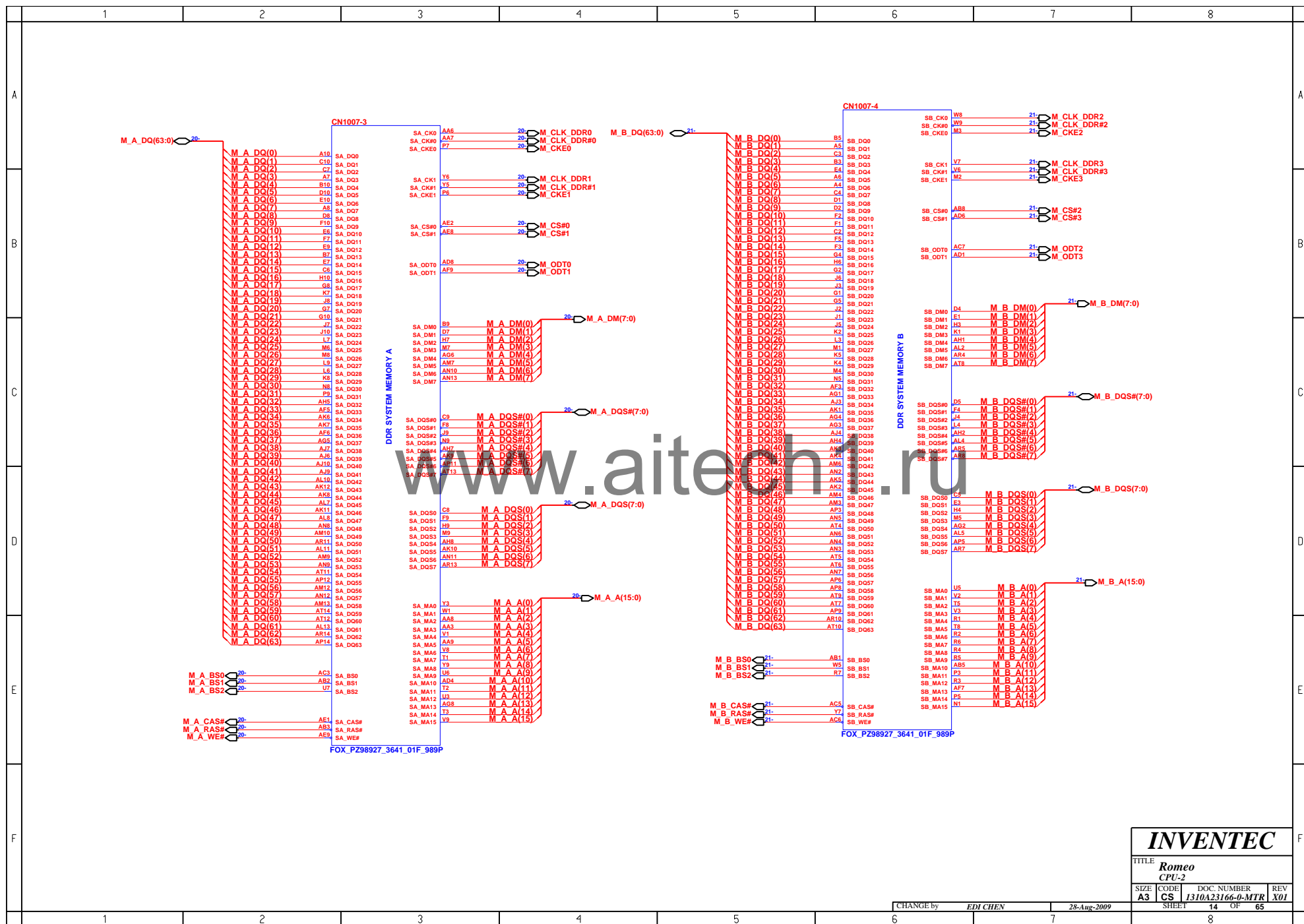




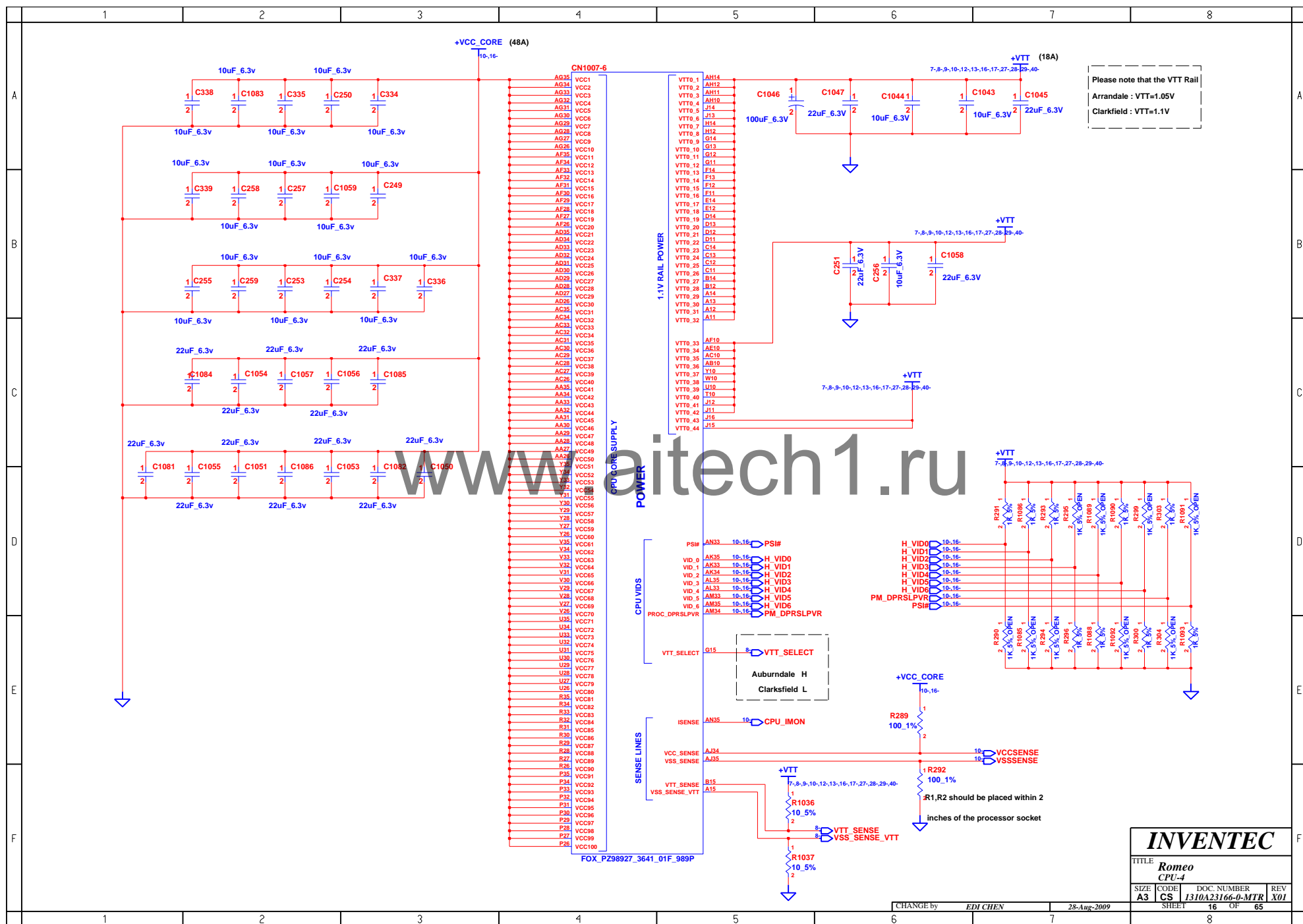
CHANGE by	<i>EDI CHEN</i>	<i>28-Aug-2009</i>
-----------	-----------------	--------------------

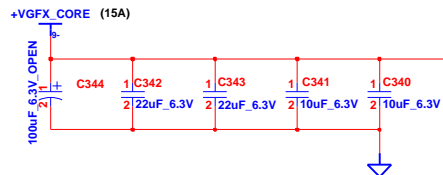
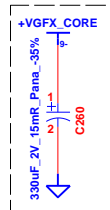




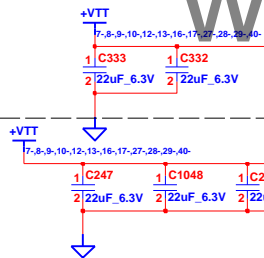






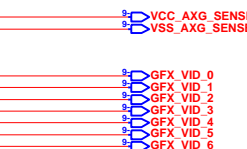
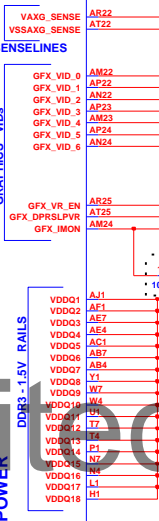


Please note that the VTT Rail Values are  
Auburndale VTT=1.05V  
Clarksfield VTT=1.1V

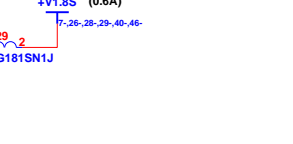
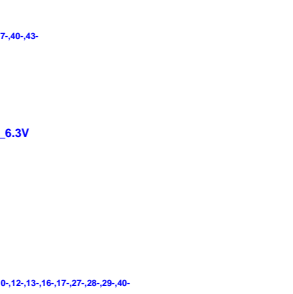
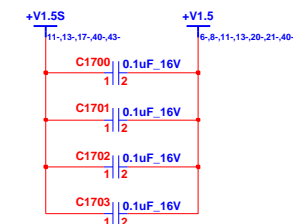
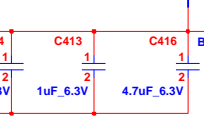
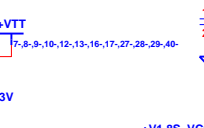
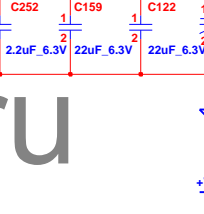
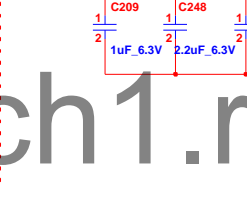
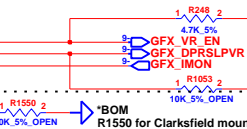


CN1007-7

SENSELINES  
GRAPHICS\_VIDs  
POWER  
DDR3-1.5V RAILS  
PEG & DMI



Route VCC\_AXG\_SENSE and VSS\_AXG\_SENSE differentially

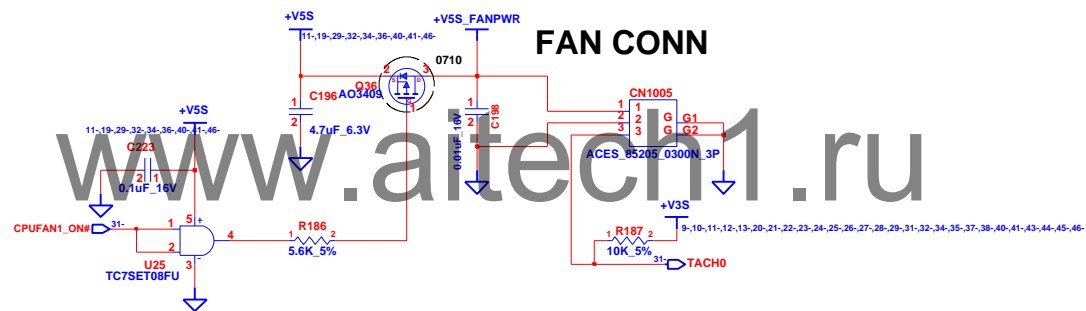


**INVENTEC**

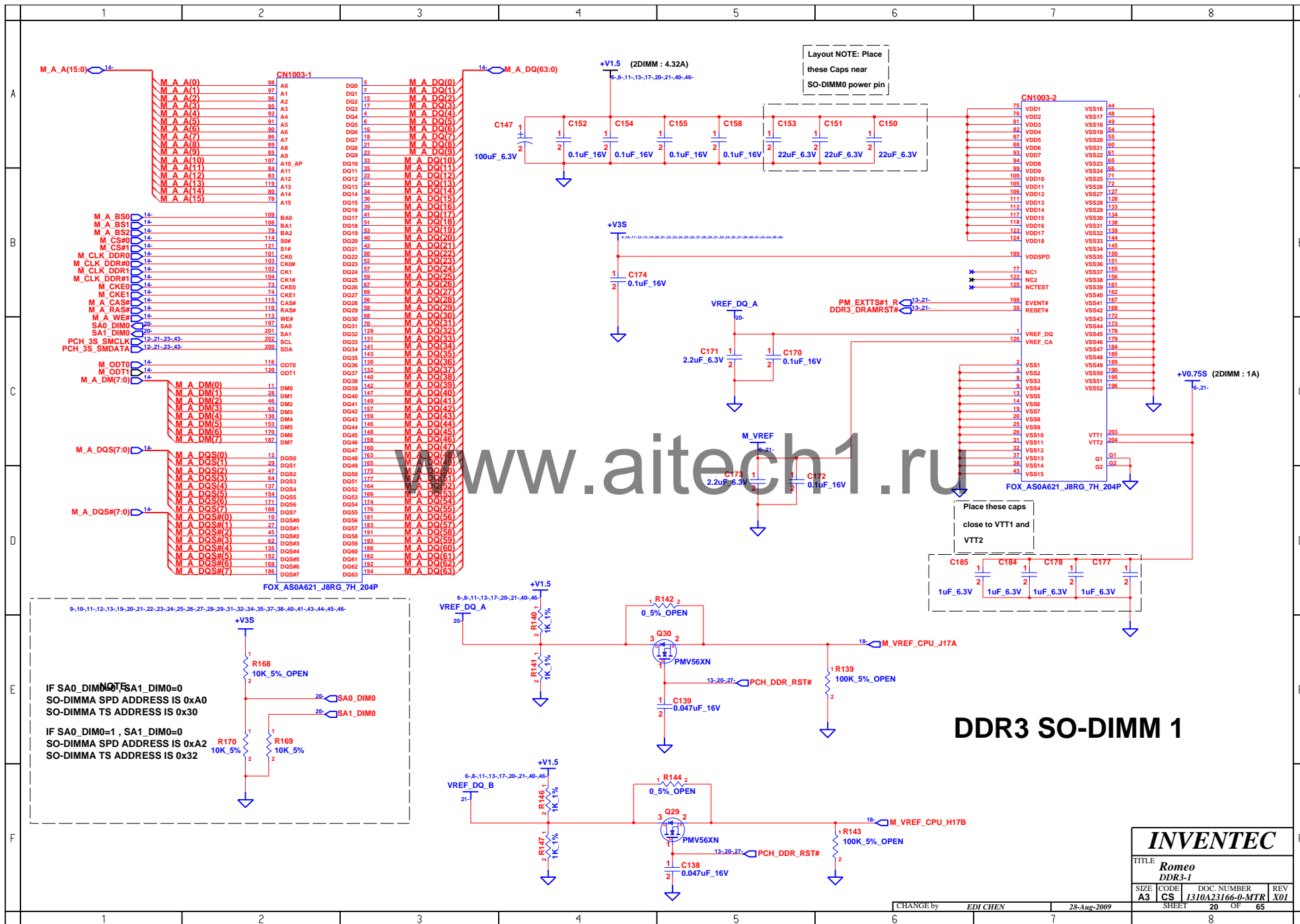
TITLE <b>Romeo CPU-5</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
SHEET <b>17</b>		OF <b>65</b>	

CHANGE by **EDI CHEN** 28-Aug-2009





INVENTEC			
TITLE <b>Romeo</b> Thermal & Fan			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
CHANGE by <b>EDI CHEN</b>		28-Aug-2009	
SHEET		19 OF 65	

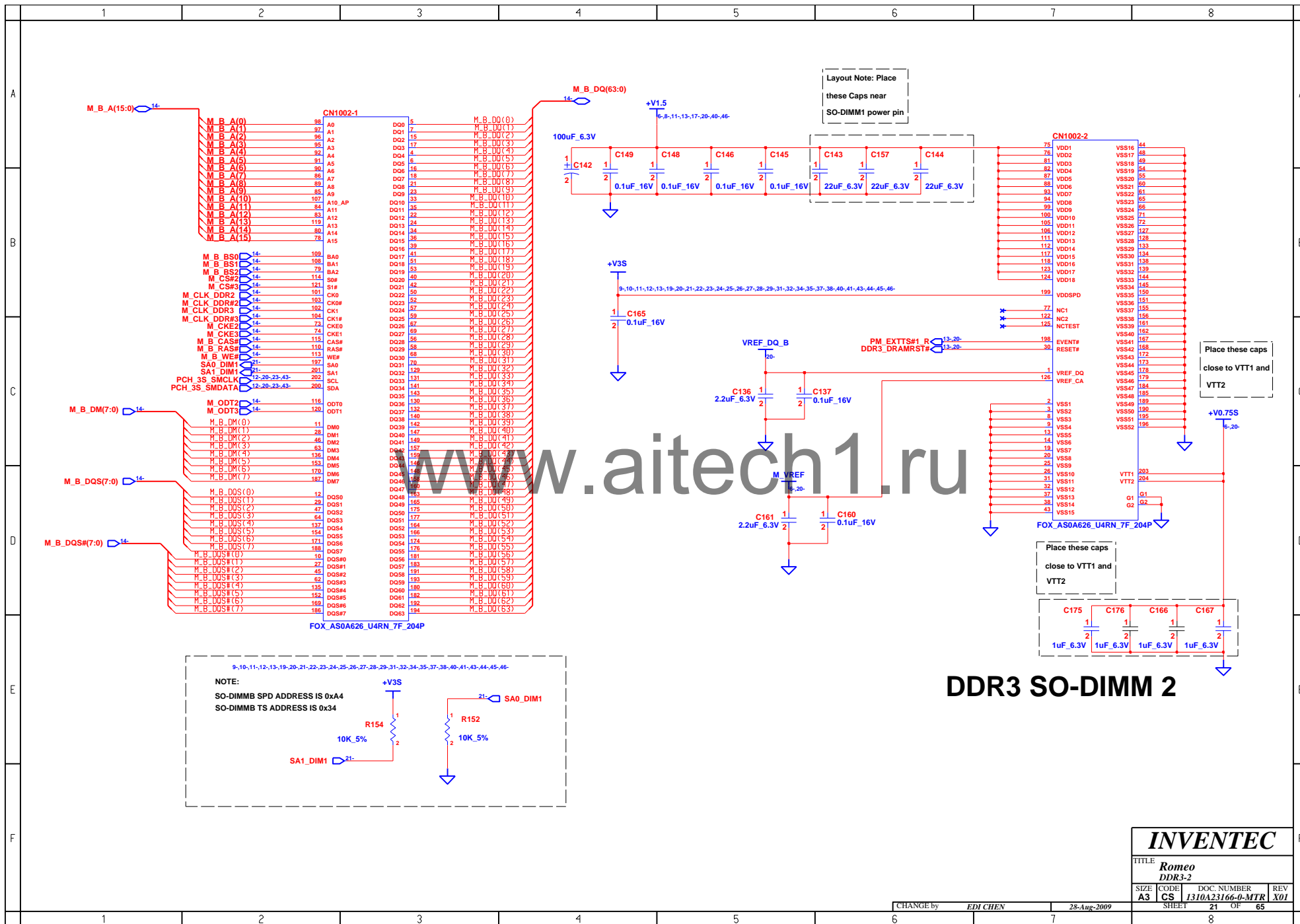


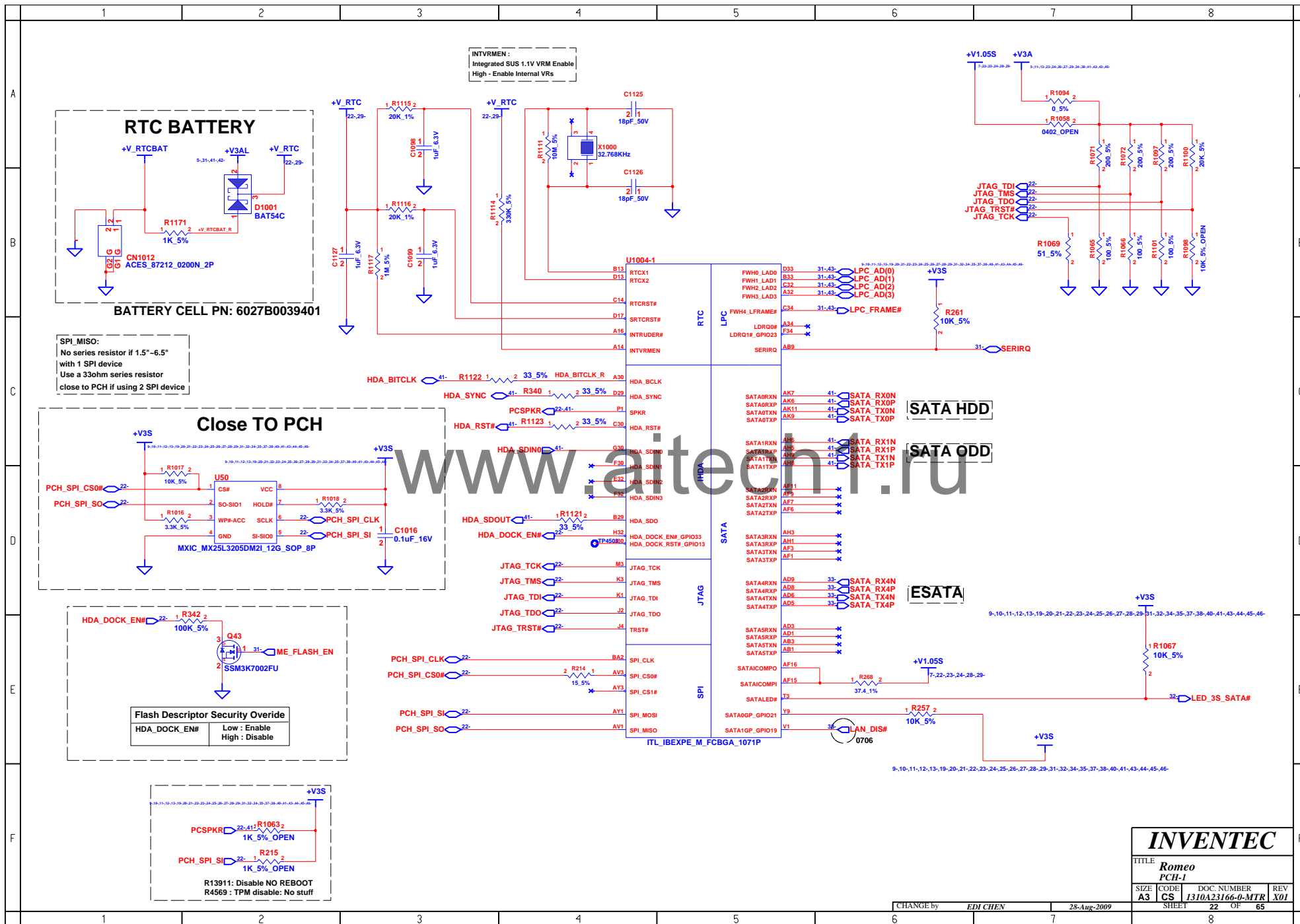
## DDR3 SO-DIMM 1

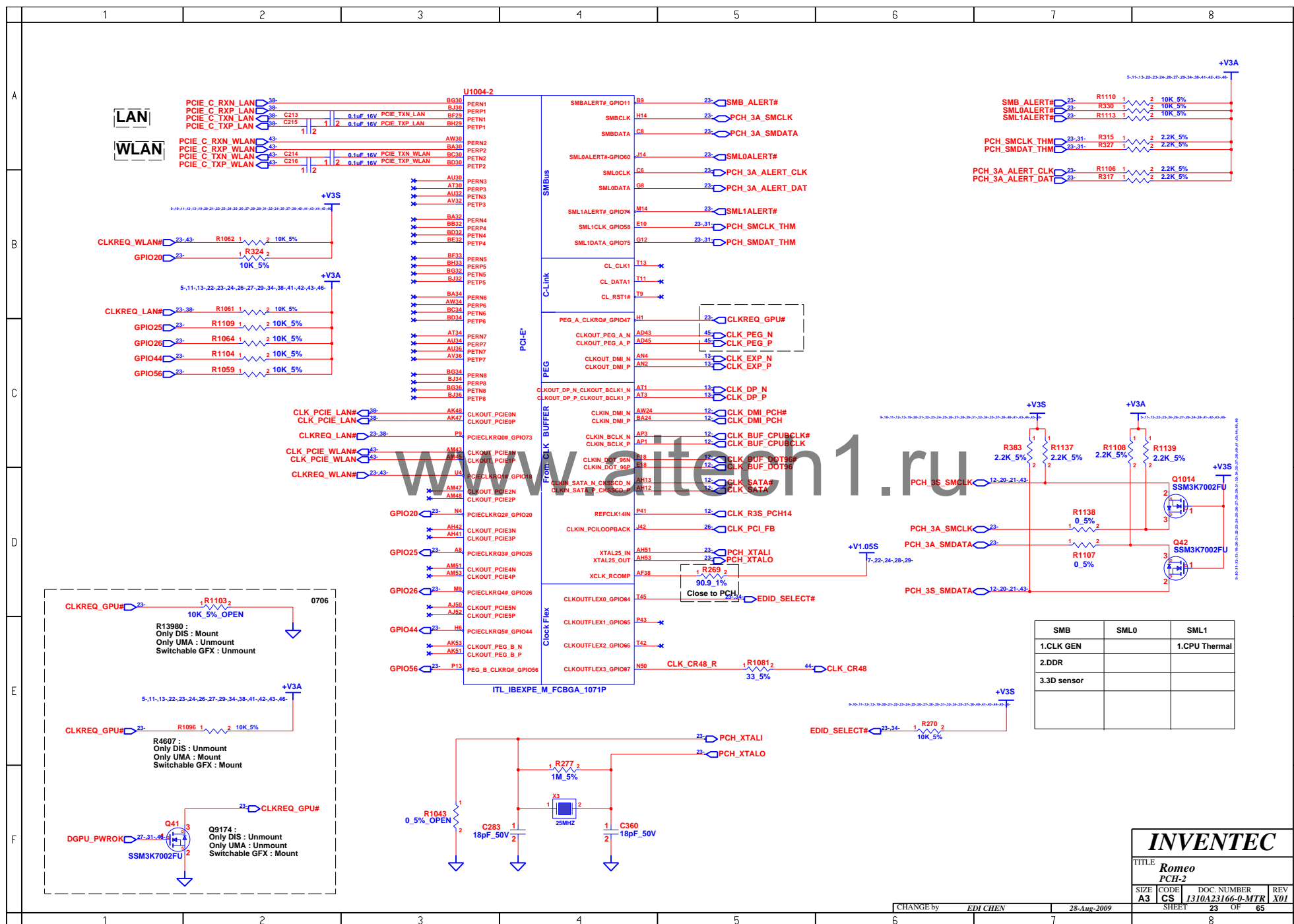
**INVENTEC**

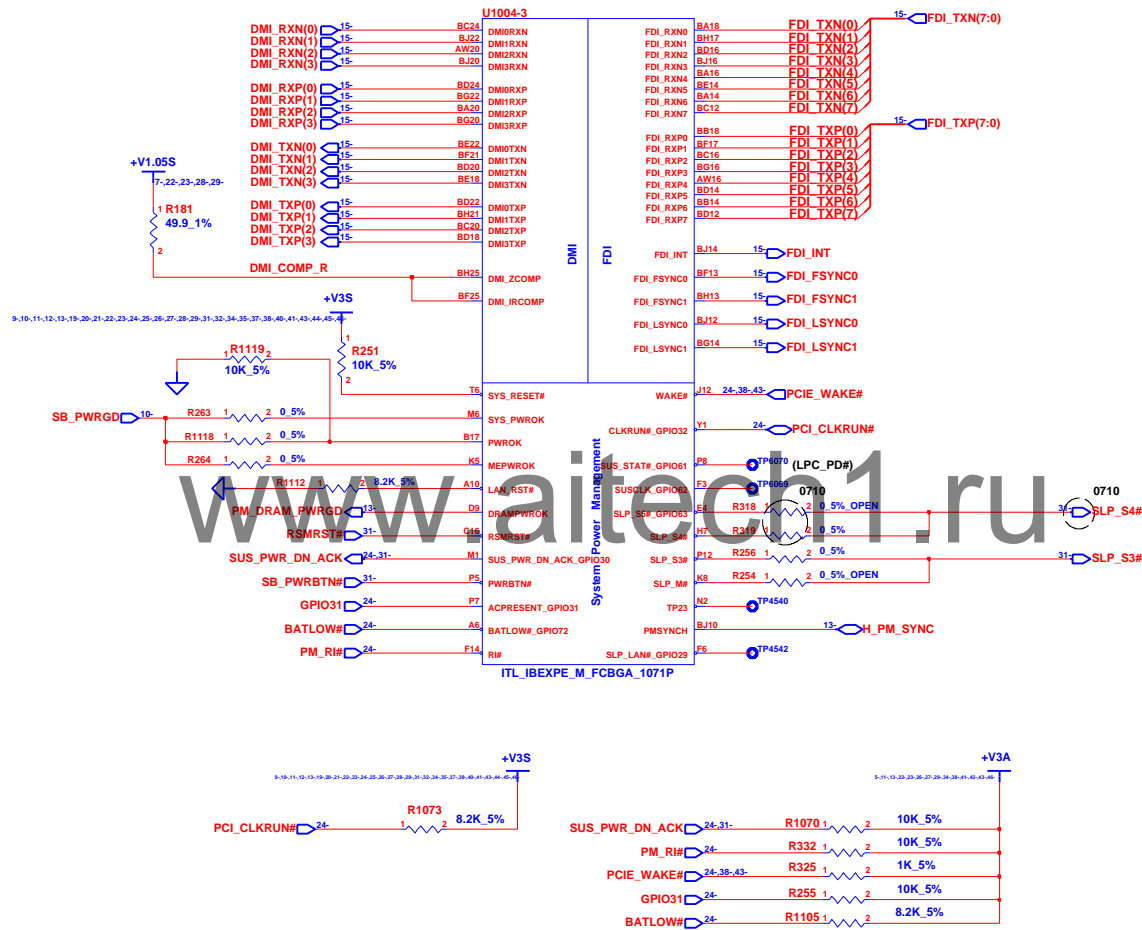
TITLE Romeo DDR3-1			
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV X01
SHEET 20		OF 65	







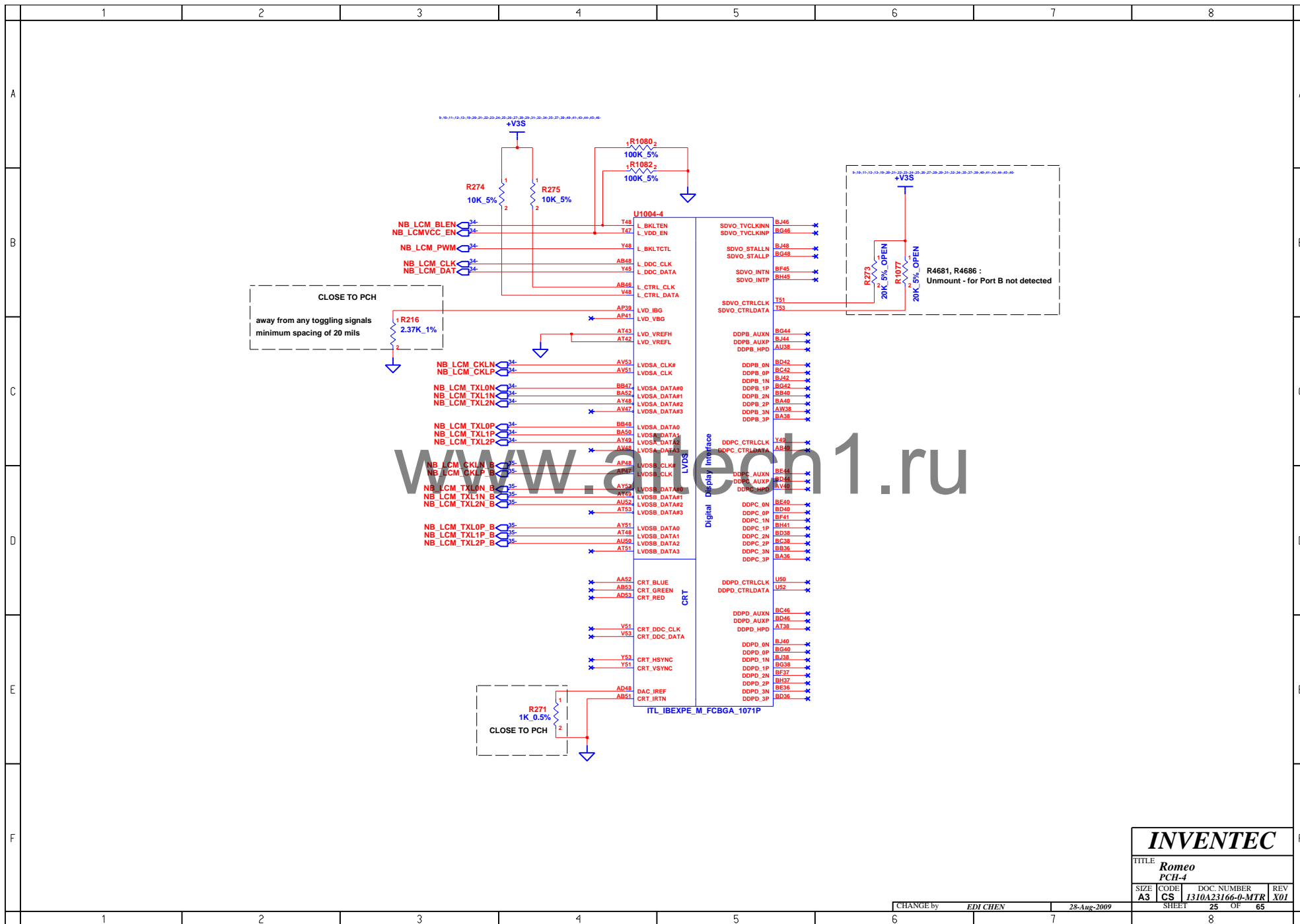


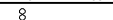


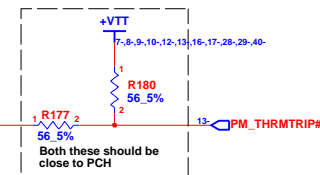
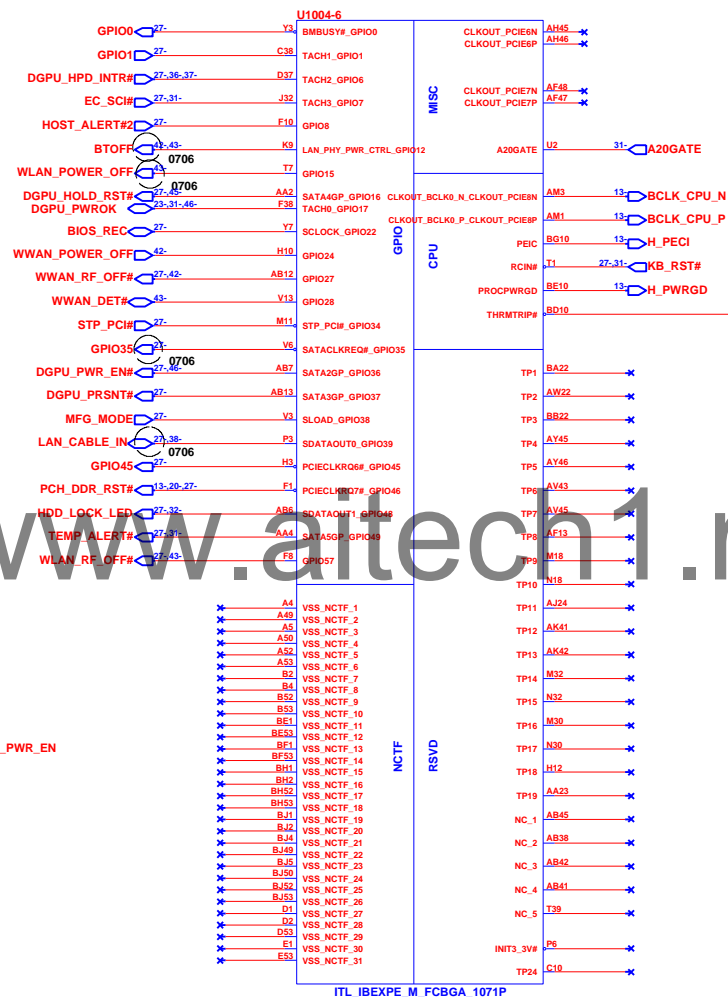
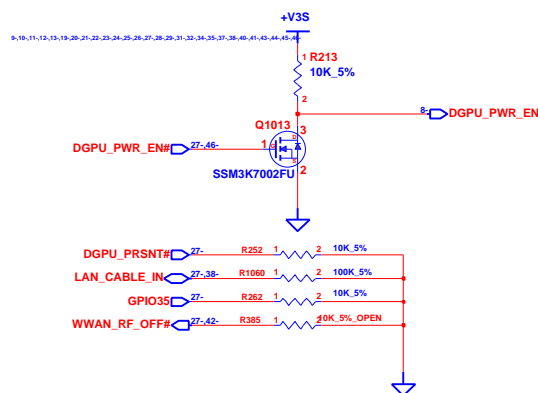
**INVENTEC**

TITLE <b>Romeo PCH-3</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
SHEET <b>24</b>		OF <b>65</b>	

CHANGE by **EDI CHEN** 28-Aug-2009







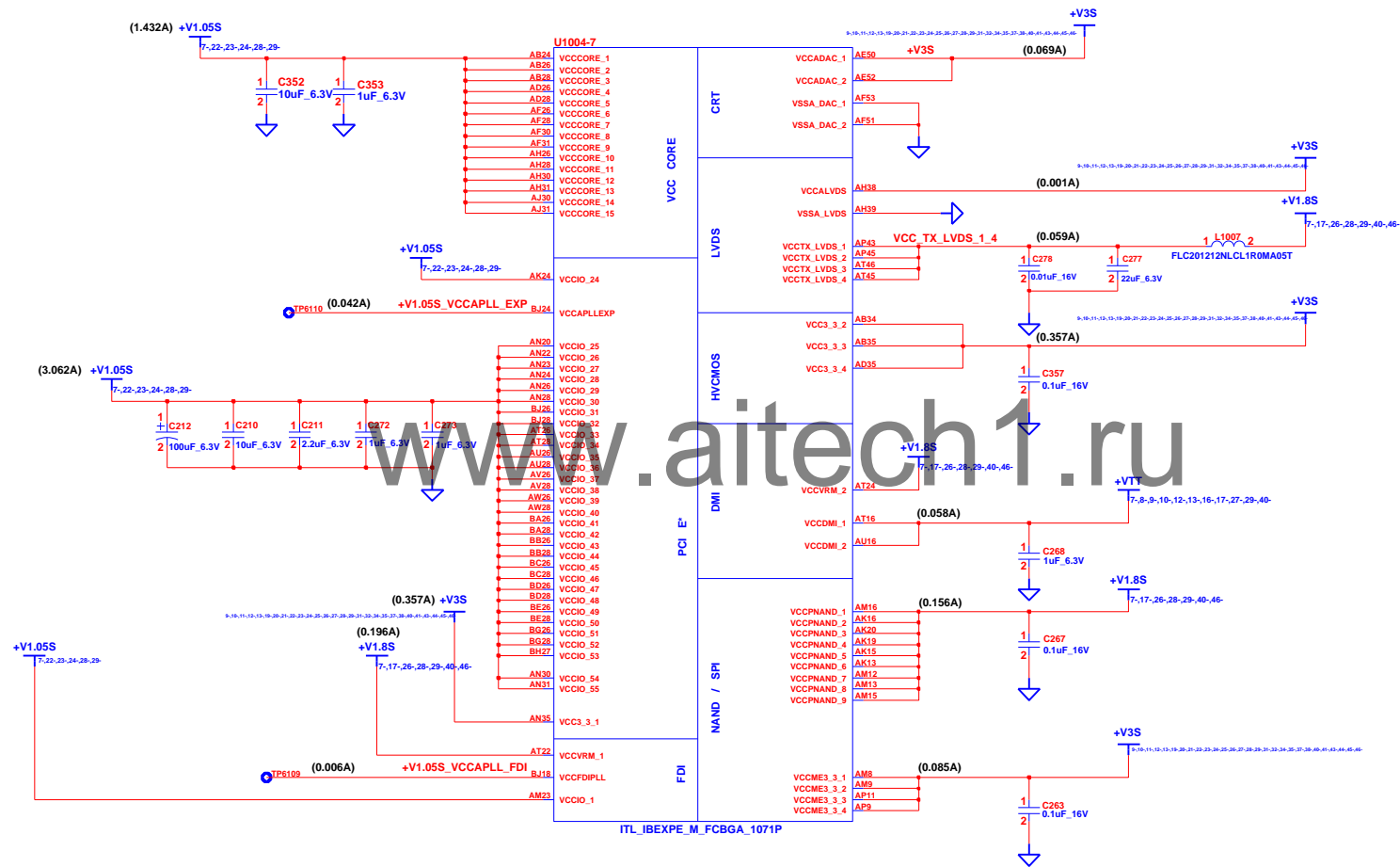
Both these should be

PCH\_DDR\_RST# 13-20-27- F1 PCIECLKREQ# GPIO46 TP6 AV43  
 HDD\_LOCK\_LED 27-32- AB8 SATAA0UT1 GPIO48 TP7 AV45  
 TEMP\_ALERT# 27-37- AA4 SATAS0# GPIO49 TP8 AF13  
 WLAN\_RF\_OFF# 27-43- F8 GPIO57 TP9 M18  
 TP10 M18

TITLE			
<b>Romeo</b> <b>PCH-6</b>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b>1310A23166-0-MTR</b>	<b>X01</b>
SHEET		27	OF 65

CHANGE by	<i>EDI CHEN</i>	28-Aug-2009
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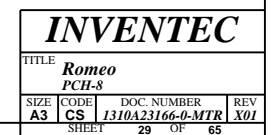
8



INVENTEC				
TITLE Romeo PCH-7				
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV X01	
SHEET		28	OF	65

CHANGE by EDI CHEN 28-Aug-2009





	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

**U1004-9**

AB16 VSS\_0

AA19 VSS\_1

AA20 VSS\_2

AA22 VSS\_3

AM19 VSS\_4

AA24 VSS\_5

AA26 VSS\_6

AA28 VSS\_7

AA30 VSS\_8

AA31 VSS\_9

AA32 VSS\_10

AB11 VSS\_11

AB15 VSS\_12

AB23 VSS\_13

AB30 VSS\_14

AB32 VSS\_15

AB33 VSS\_16

AB39 VSS\_17

AB43 VSS\_18

AB47 VSS\_19

AB5 VSS\_20

AB8 VSS\_21

AC2 VSS\_22

AC52 VSS\_23

AD11 VSS\_24

AD12 VSS\_25

AD16 VSS\_26

AD20 VSS\_27

AD30 VSS\_28

AD31 VSS\_29

AD32 VSS\_30

AD34 VSS\_31

AU22 VSS\_32

AD42 VSS\_33

AD48 VSS\_34

AD7 VSS\_35

AE2 VSS\_36

AE4 VSS\_37

AF12 VSS\_38

Y13 VSS\_39

Y13 VSS\_40

AH49 VSS\_41

AU4 VSS\_42

AF35 VSS\_43

AP13 VSS\_44

AN24 VSS\_45

AF45 VSS\_46

AF46 VSS\_47

AF49 VSS\_48

AF5 VSS\_49

AF8 VSS\_50

AQ2 VSS\_51

AQ52 VSS\_52

AH11 VSS\_53

AH15 VSS\_54

AH16 VSS\_55

AH24 VSS\_56

AH32 VSS\_57

AV18 VSS\_58

AH43 VSS\_59

AH7 VSS\_60

AJ19 VSS\_61

AJ2 VSS\_62

AJ2 VSS\_63

AJ20 VSS\_64

AJ22 VSS\_65

AJ23 VSS\_66

AJ26 VSS\_67

AJ28 VSS\_68

AJ32 VSS\_69

AJ34 VSS\_70

AJ5 VSS\_71

AJ4 VSS\_72

AK12 VSS\_73

AM41 VSS\_74

AM19 VSS\_75

AK26 VSS\_76

AK22 VSS\_77

AK23 VSS\_78

AK28 VSS\_79

AK30 VSS\_80

AK31 VSS\_81

AK32 VSS\_82

AK34 VSS\_83

AK35 VSS\_84

AK38 VSS\_85

AK40 VSS\_86

AK40 VSS\_87

AK49 VSS\_88

AK5 VSS\_89

AK8 VSS\_90

AL2 VSS\_91

AL52 VSS\_92

AM11 VSS\_93

AD24 VSS\_94

AM20 VSS\_95

AM22 VSS\_96

AM24 VSS\_97

AM26 VSS\_98

AM28 VSS\_99

BA42 VSS\_100

AM20 VSS\_101

AM31 VSS\_102

AM32 VSS\_103

AM34 VSS\_104

AM35 VSS\_105

AM38 VSS\_106

AM39 VSS\_107

AM42 VSS\_108

AU20 VSS\_109

AM46 VSS\_110

AV22 VSS\_111

AM49 VSS\_112

AM7 VSS\_113

AA50 VSS\_114

AA50 VSS\_115

BB10 VSS\_116

AN32 VSS\_117

AN50 VSS\_118

AN52 VSS\_119

AP12 VSS\_120

AP12 VSS\_121

AP46 VSS\_122

AP49 VSS\_123

AP5 VSS\_124

AP5 VSS\_125

AP5 VSS\_126

AP52 VSS\_127

AT11 VSS\_128

BA12 VSS\_129

AM48 VSS\_130

AT32 VSS\_131

AT36 VSS\_132

AT41 VSS\_133

AT47 VSS\_134

AT7 VSS\_135

AV12 VSS\_136

AV16 VSS\_137

AV20 VSS\_138

AV24 VSS\_139

AV30 VSS\_140

AV34 VSS\_141

AV38 VSS\_142

AV38 VSS\_143

AV40 VSS\_144

AV46 VSS\_145

AV5 VSS\_146

AV8 VSS\_147

AW14 VSS\_148

AW18 VSS\_149

AW2 VSS\_150

BP9 VSS\_151

AW32 VSS\_152

AW36 VSS\_153

AW40 VSS\_154

AW52 VSS\_155

AY11 VSS\_156

AY43 VSS\_157

AY47 VSS\_158

ITL\_IBEXPE\_M\_FCBGA\_1071P

**U1004-10**

AY7 VSS\_159

B11 VSS\_160

B15 VSS\_161

B19 VSS\_162

B23 VSS\_163

B31 VSS\_164

B35 VSS\_165

B39 VSS\_166

B43 VSS\_167

B47 VSS\_168

B7 VSS\_169

BG12 VSS\_170

BB12 VSS\_171

BB14 VSS\_172

BB20 VSS\_173

BB24 VSS\_174

BB30 VSS\_175

BB34 VSS\_176

BB38 VSS\_177

BB42 VSS\_178

BB49 VSS\_179

BB5 VSS\_180

BC10 VSS\_181

BC14 VSS\_182

BC18 VSS\_183

BC2 VSS\_184

BC22 VSS\_185

BC32 VSS\_186

BC46 VSS\_187

BC40 VSS\_188

BC44 VSS\_189

BC52 VSS\_190

BD9 VSS\_191

BD48 VSS\_192

BD49 VSS\_193

BD5 VSS\_194

BE14 VSS\_195

BE15 VSS\_196

BE20 VSS\_197

BE24 VSS\_198

BE30 VSS\_199

BE34 VSS\_200

BE38 VSS\_201

BE42 VSS\_202

BE46 VSS\_203

BE48 VSS\_204

BE50 VSS\_205

BE5 VSS\_206

BE5 VSS\_207

BE5 VSS\_208

BE5 VSS\_209

BE5 VSS\_210

BE5 VSS\_211

BE5 VSS\_212

BE5 VSS\_213

BE5 VSS\_214

BE5 VSS\_215

BE5 VSS\_216

BE5 VSS\_217

BE5 VSS\_218

BE5 VSS\_219

BE5 VSS\_220

BE5 VSS\_221

BE5 VSS\_222

BE5 VSS\_223

BE5 VSS\_224

C12 VSS\_225

C50 VSS\_226

D51 VSS\_227

E12 VSS\_228

E16 VSS\_229

E20 VSS\_230

E24 VSS\_231

E30 VSS\_232

E34 VSS\_233

E38 VSS\_234

E42 VSS\_235

E46 VSS\_236

E49 VSS\_237

E5 VSS\_238

E9 VSS\_239

F49 VSS\_240

F5 VSS\_241

G10 VSS\_242

G14 VSS\_243

G18 VSS\_244

G2 VSS\_245

G22 VSS\_246

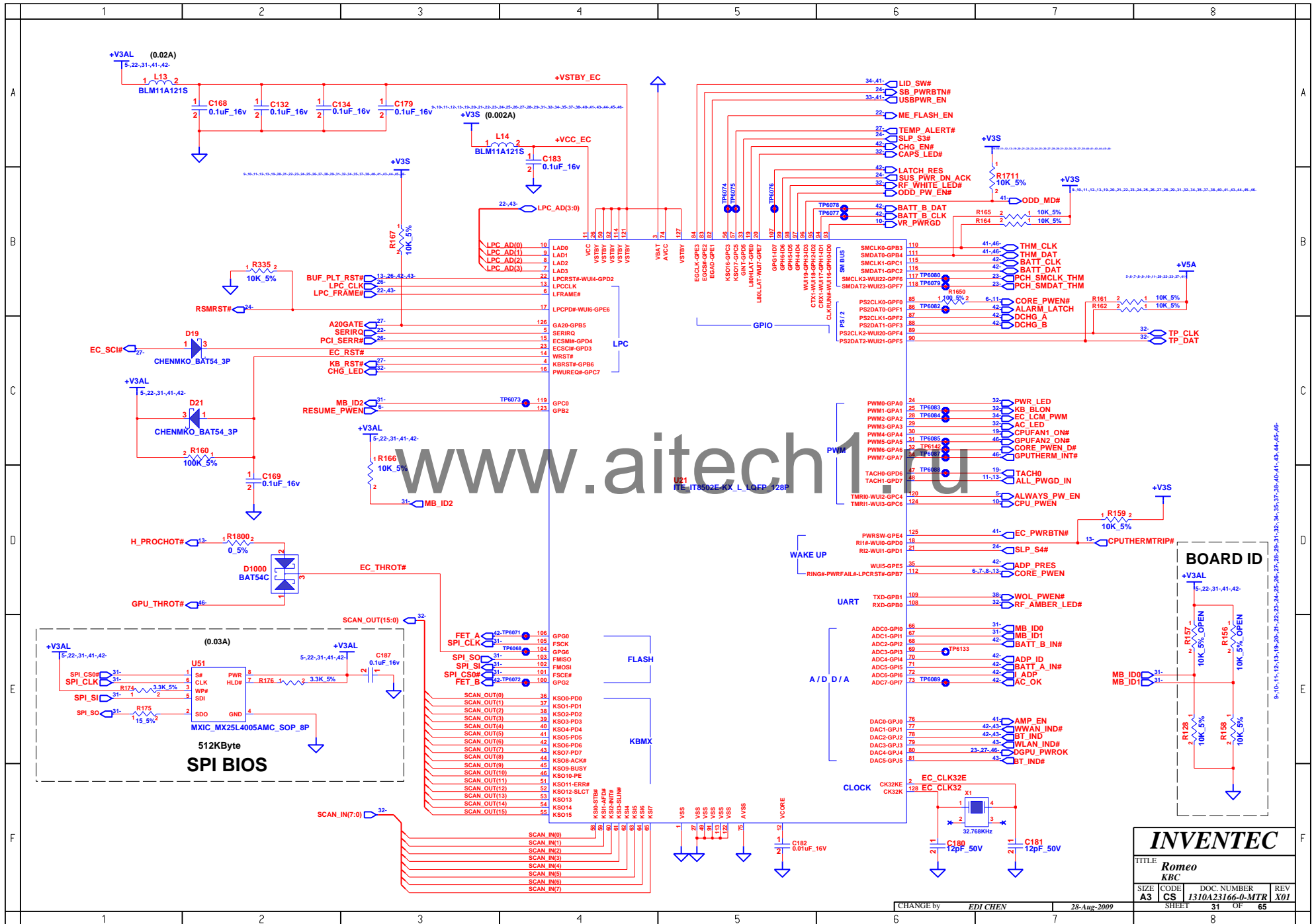
G32 VSS\_247

G36 VSS\_248

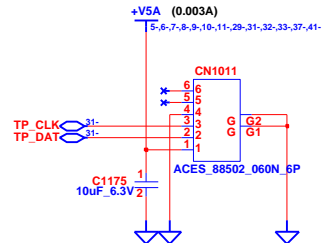
G40 VSS\_249

G44 VSS\_250

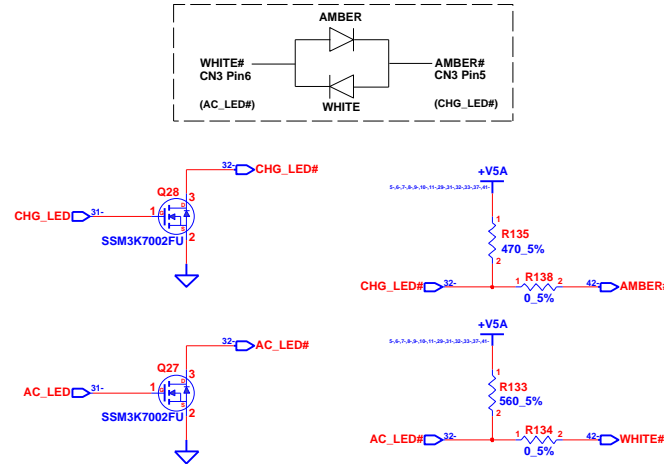
G52 VSS\_



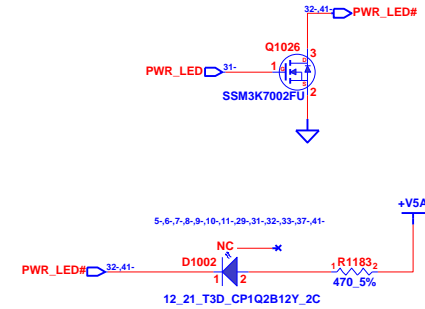
## TouchPad Module CONN



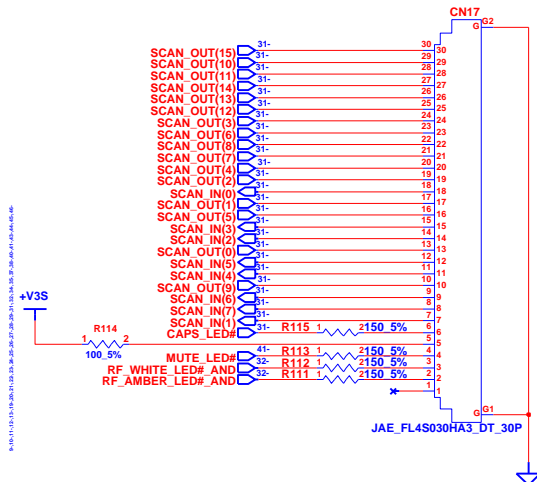
## DC-JACK LED



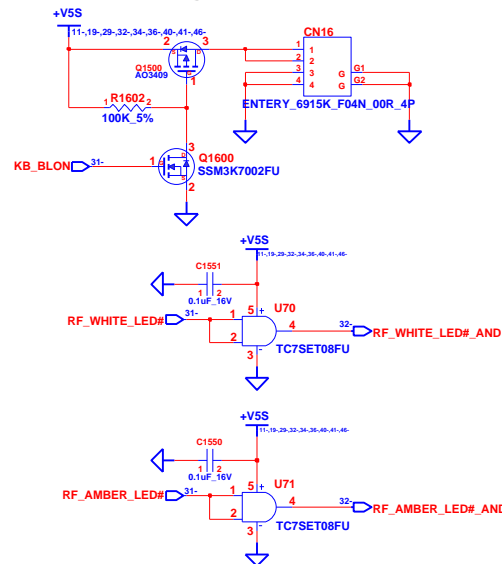
## POWER LED



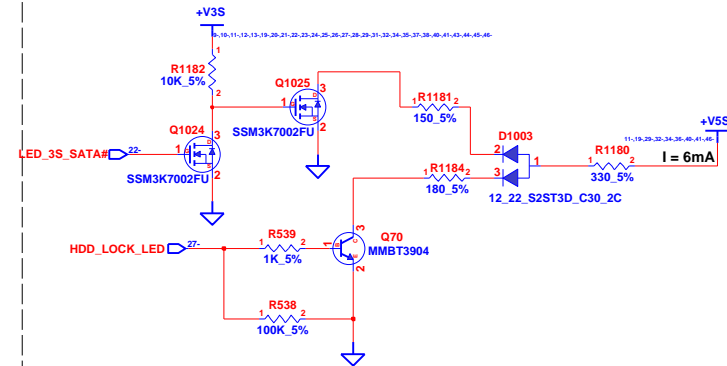
## KeyBoard CONN (30 pin)



## Backlight connector



## SATA LED & HDD-HALTED LED

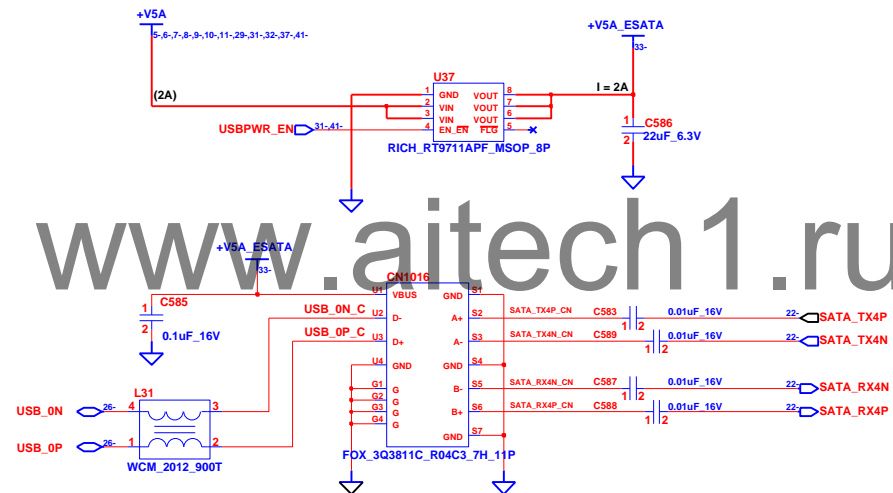


**INVENTEC**

TITLE			
Romeo			
Keyboard & Touchpad			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		32	OF 65

CHANGE by EDI CHEN

28-Aug-2009

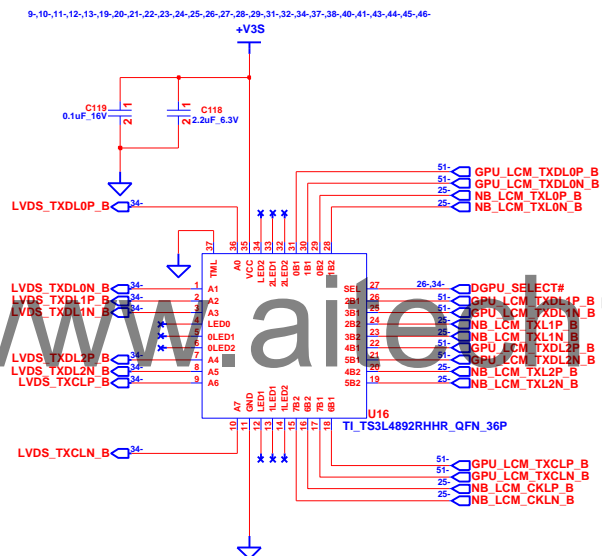


TITLE			
<b>Romeo</b> <b>CRT</b>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b>1310A23166-0-MTR</b>	<b>X01</b>
SHEET		33	OF 65

CHANGE by	<i>EDI CHEN</i>	<i>28-Aug-2009</i>
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DGPU_SELECT#	OUTPUT
L	MADISON
H	PCH

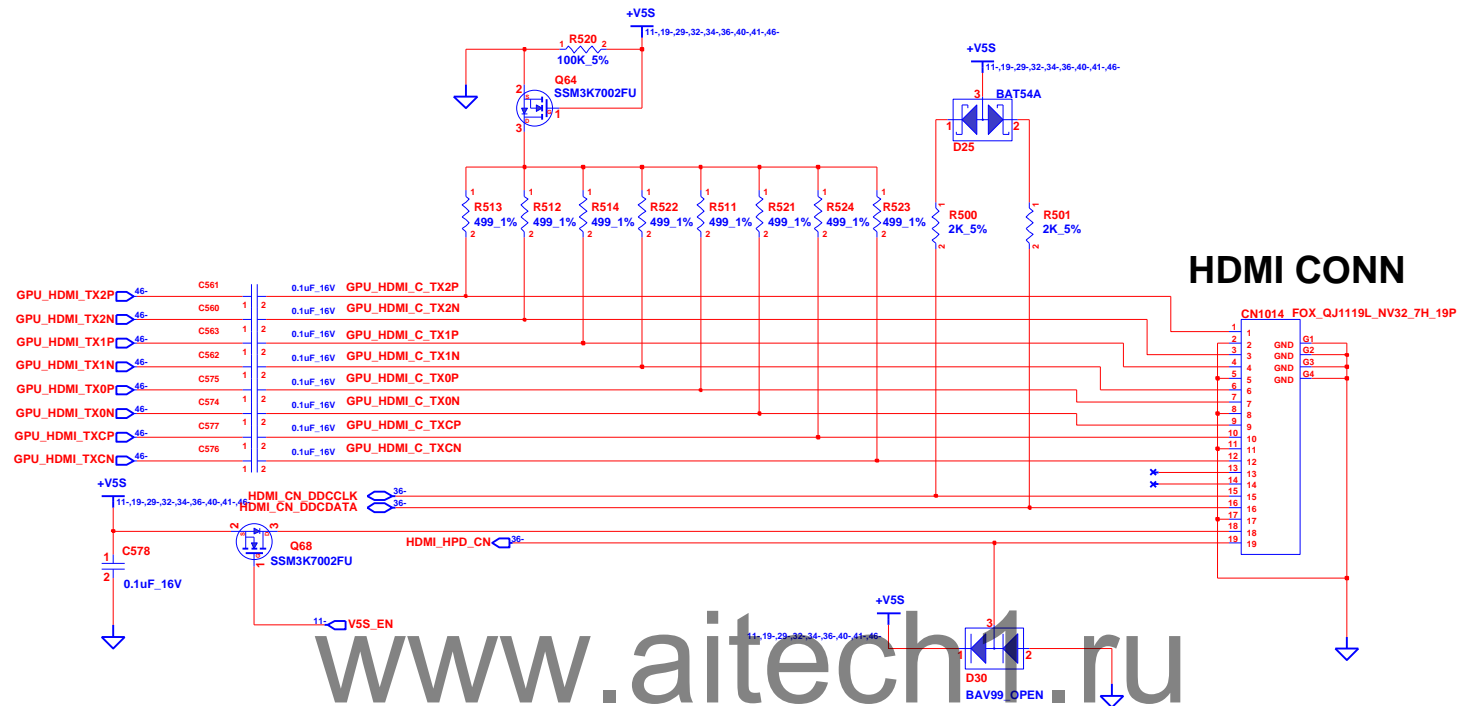
**INVENTEC**

TITLE  
**Romeo**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01

CHANGE by **EDI CHEN** 28-Aug-2009

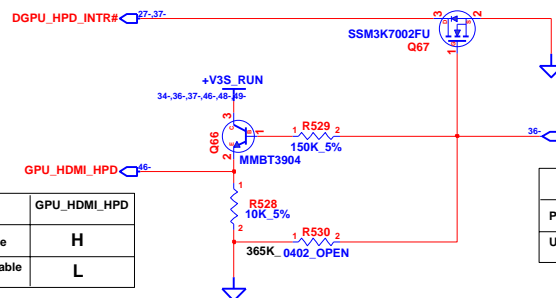
SHEET 35 OF 65



## HDMI CONN

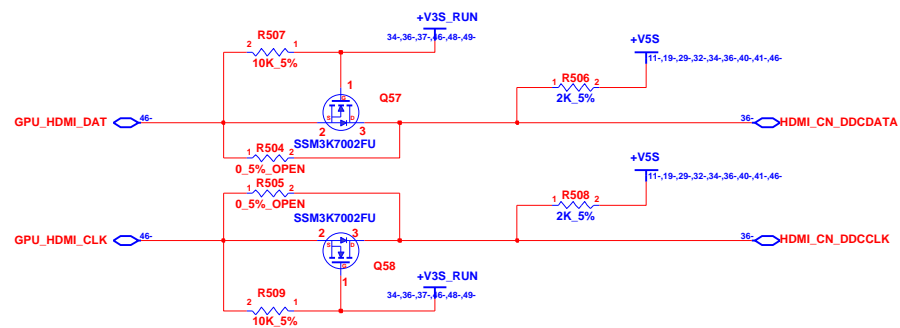
www.aitech1.ru

	DGPU_HPD_INTR#
Plug Cable	L
Unplug Cable	H



	GPU_HDMI_HPDP
Plug Cable	H
Unplug Cable	L

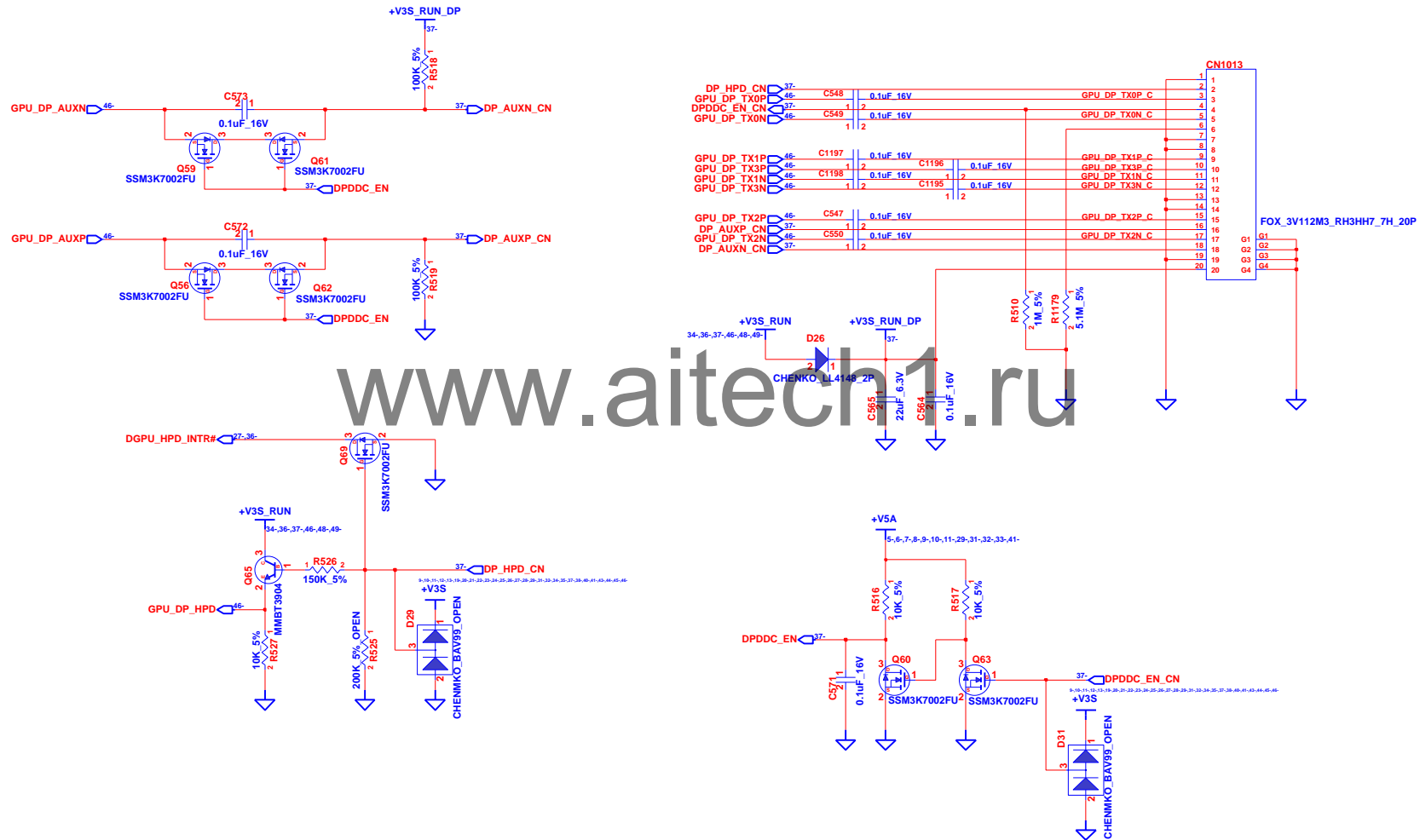
	HDMI_HPDP_CN
Plug Cable	H
Unplug Cable	L



INVENTEC			
TITLE Romeo HDMI			
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV X01
CHANGE by EDI CHEN		28-Aug-2009	
SHEET 36		OF 65	



# Display Port



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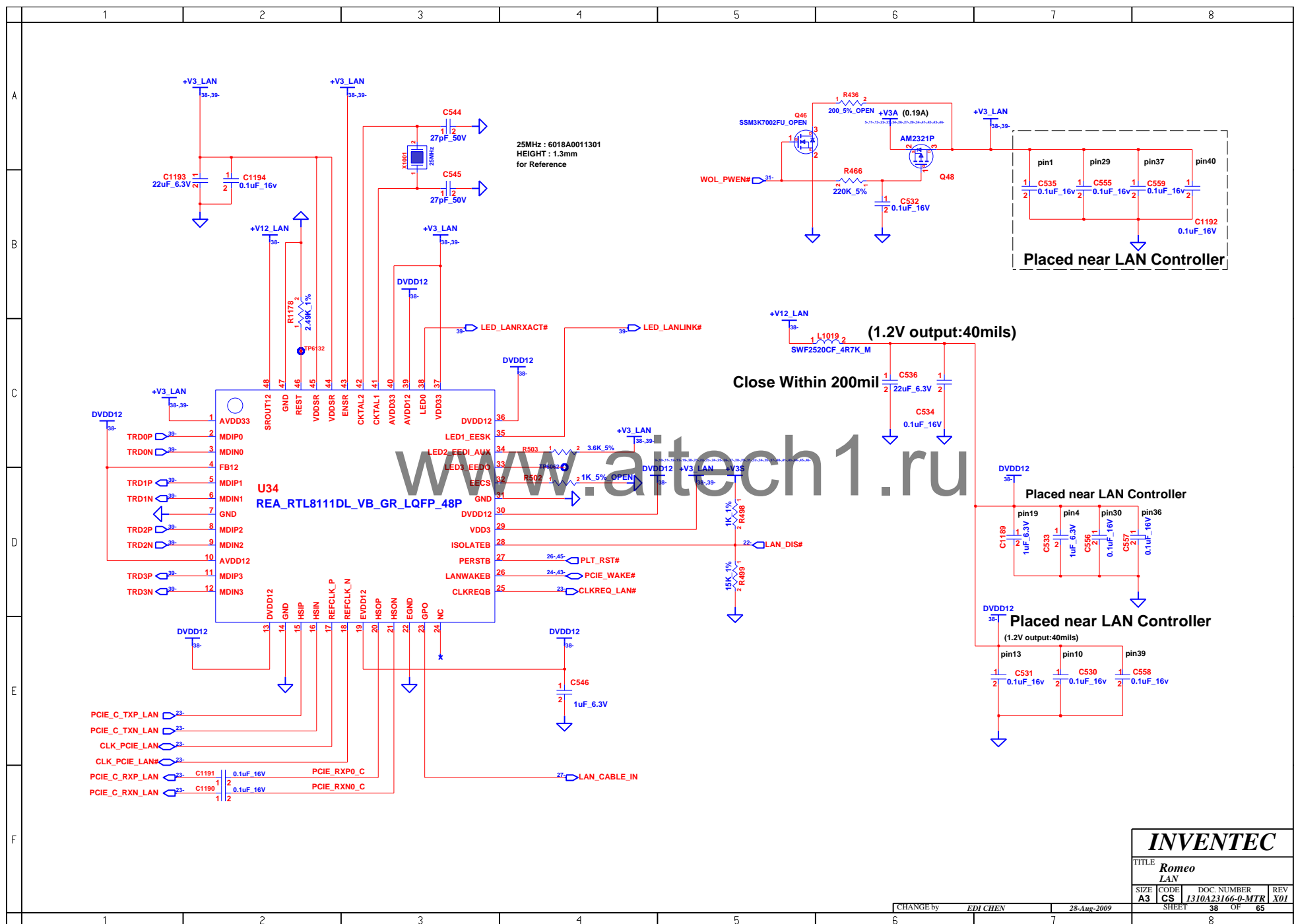
**INVENTEC**

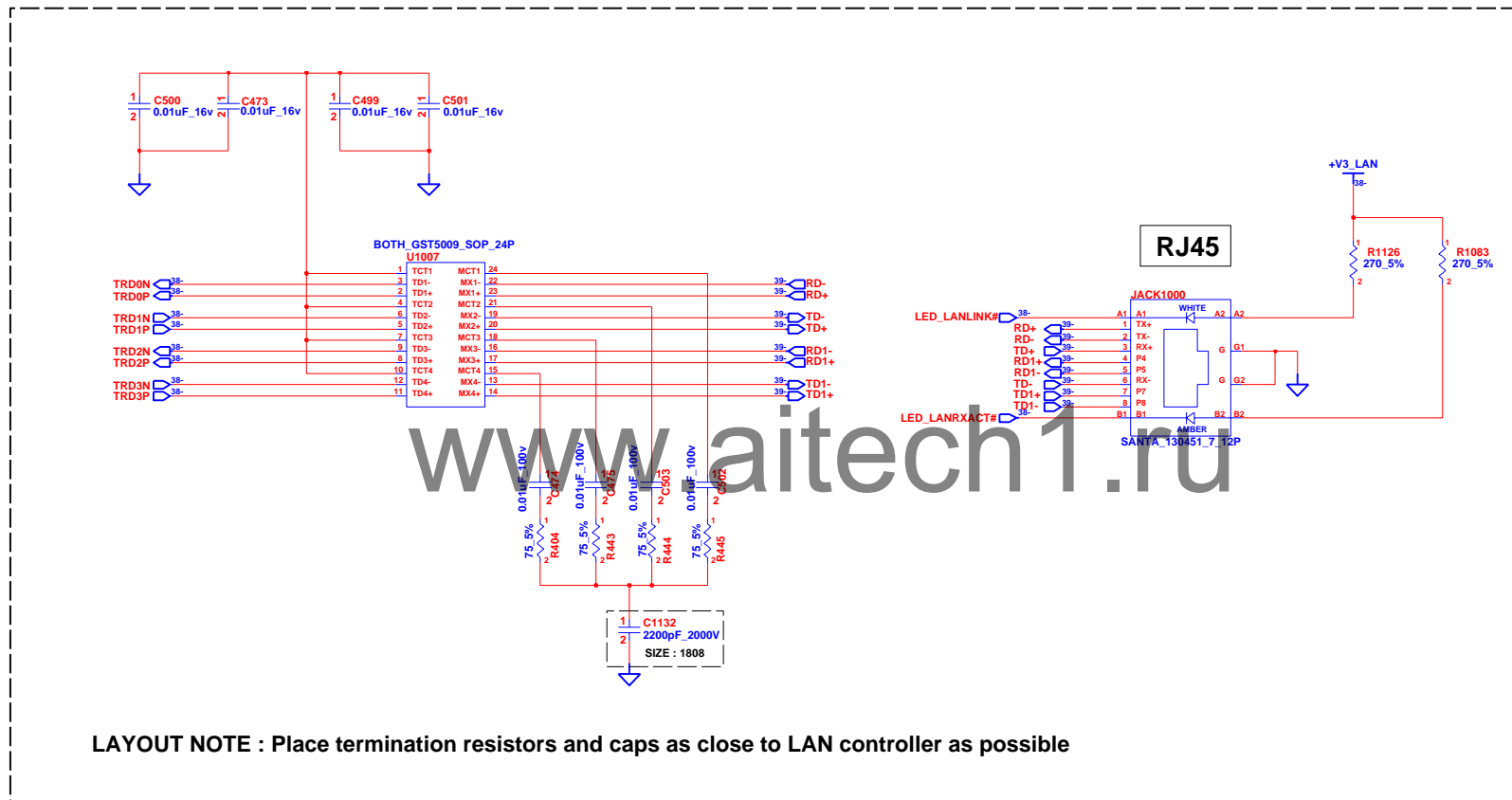
TITLE  
**Romeo DisplayPort**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
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CHANGE by **EDI CHEN** 28-Aug-2009

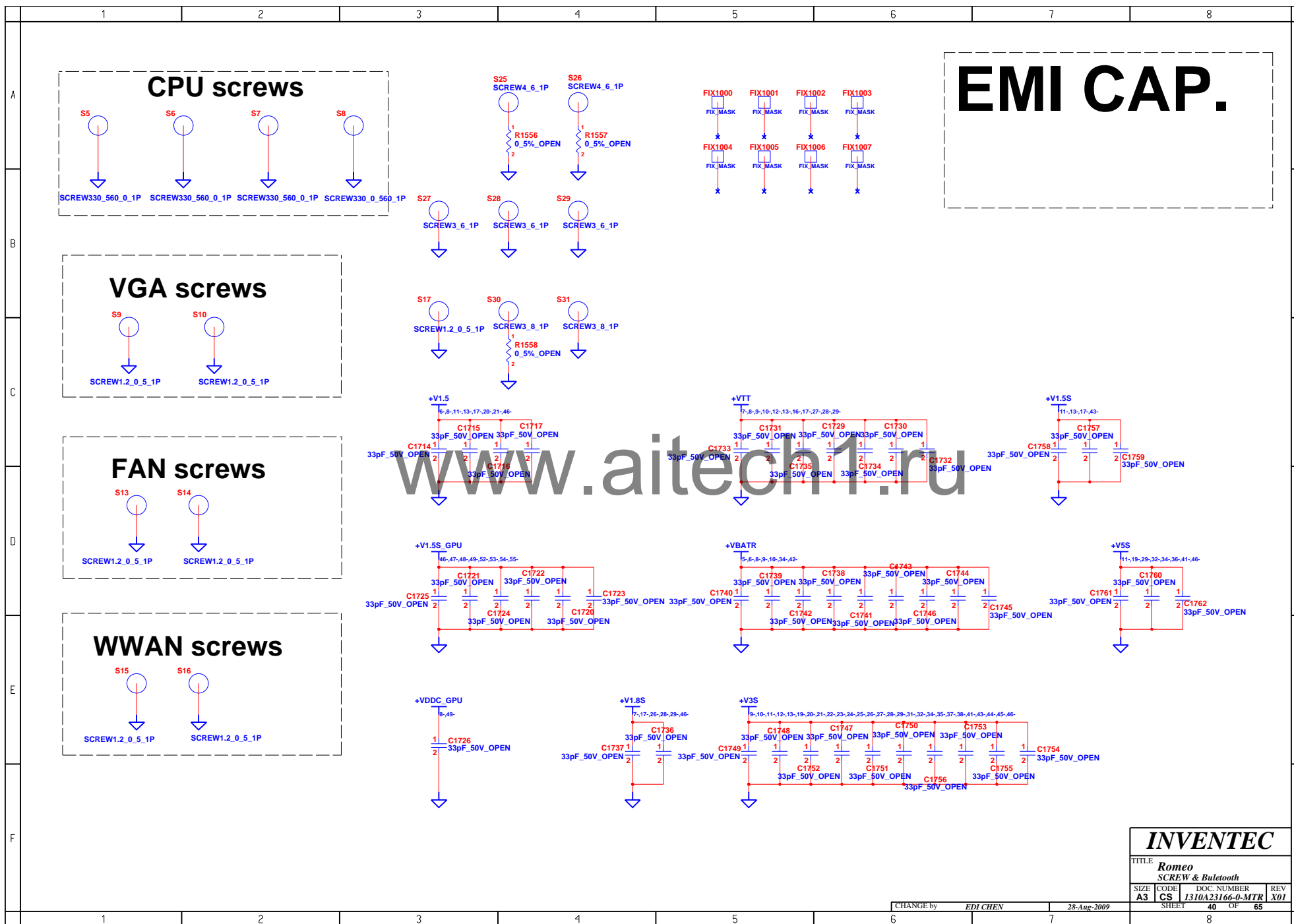
SHEET 37 OF 65





INVENTEC			
TITLE <b>Romeo</b> <b>RJ-45</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
SHEET		39	OF 65

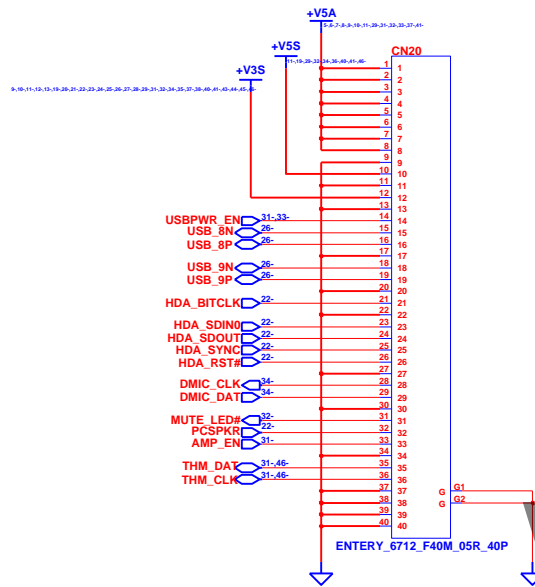
CHANGE by **EDI CHEN** 28-Aug-2009



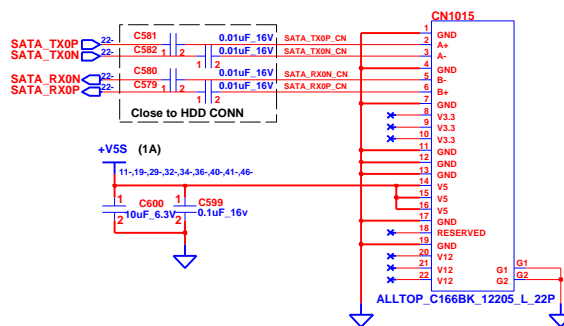
<b>INVENTEC</b>			
TITLE <b>Romeo</b>			
SCREW & Buletooth			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
SHEET <b>40</b>		OF <b>65</b>	

CHANGE by **EDI CHEN** 28-Aug-2009

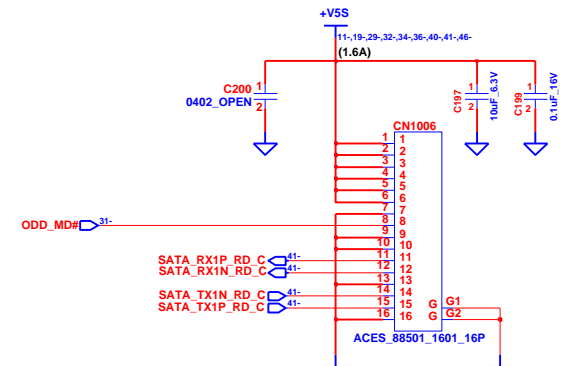
## AU Board CN on MB



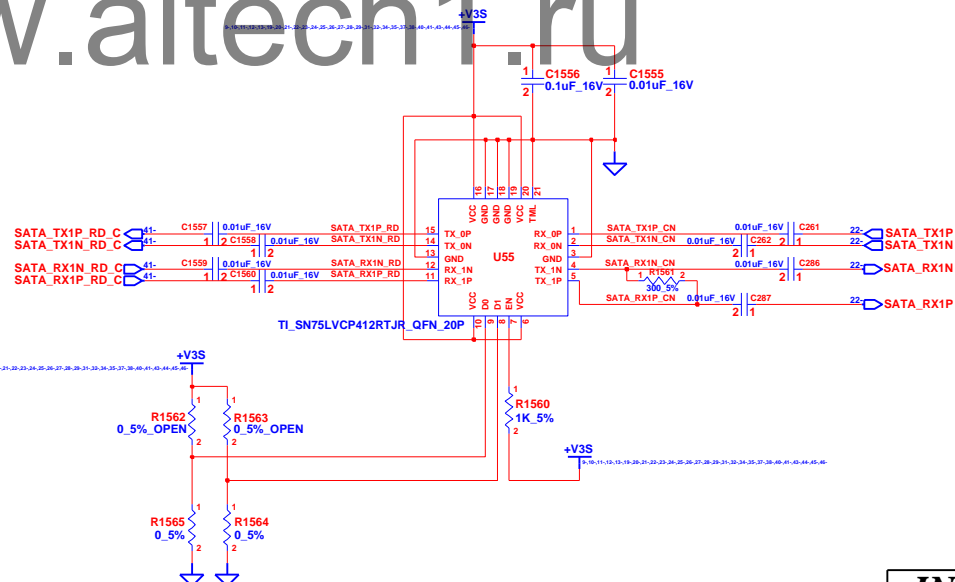
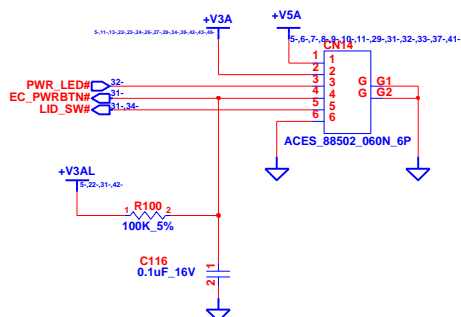
## SATA HDD CONN



## SATA ODD FFC CONN on MB



## PBN Cable CN on MB



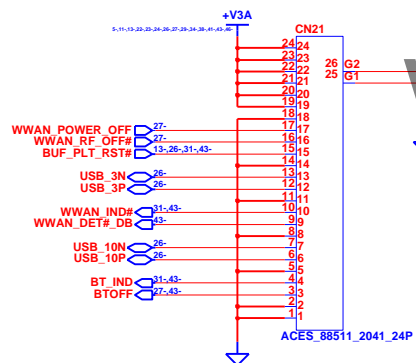
**INVENTEC**

TITLE <b>Romeo Connector-1</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
SHEET <b>41</b>		OF <b>65</b>	

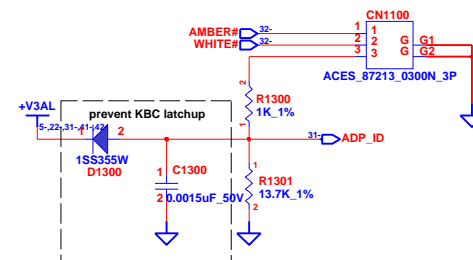
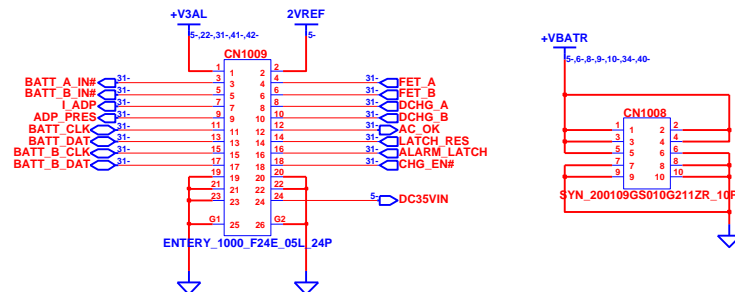
CHANGE by **EDI CHEN** 28-Aug-2009

## WC Board CN on MB

### WWAN & BT



## CHG Board CN on MB



**INVENTEC**

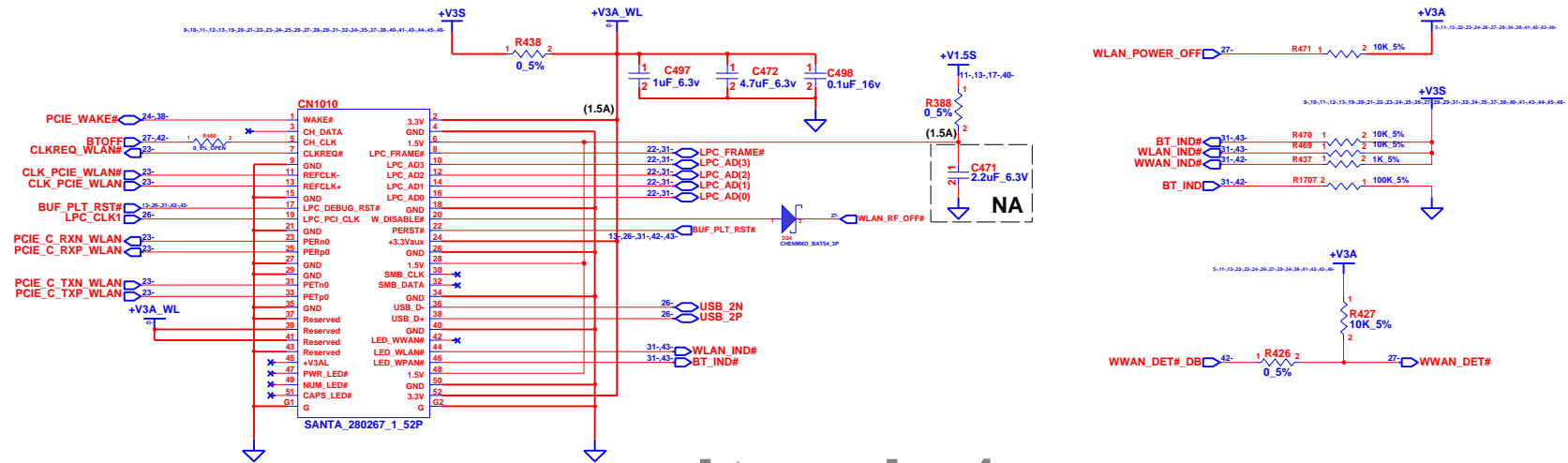
TITLE			
Romeo Connector-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		42	OF 65

CHANGE by EDI CHEN

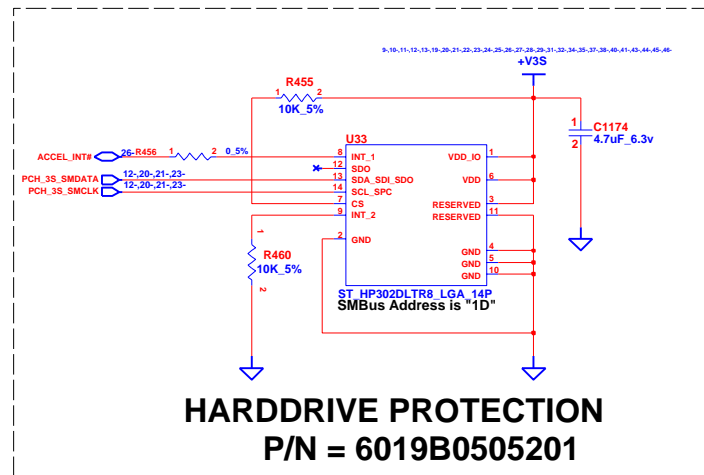
28-Aug-2009

8

# WLAN CONN (MINICARD)



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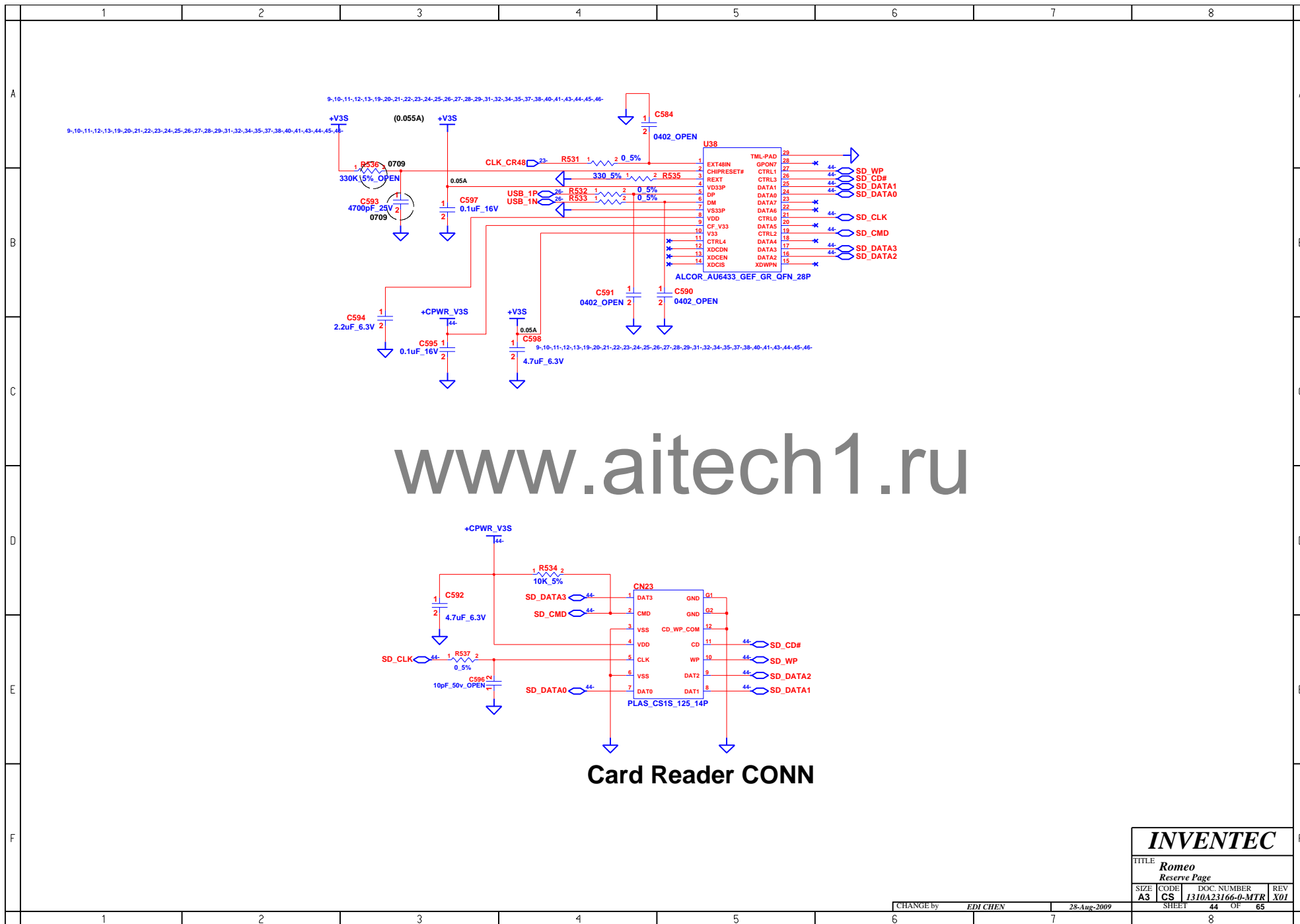


**HARDDRIVE PROTECTION**  
P/N = 6019B0505201

**INVENTEC**

TITLE			
Romeo			
WLAN & HDD Lock			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		43	OF 65

CHANGE by EDI CHEN 28-Aug-2009

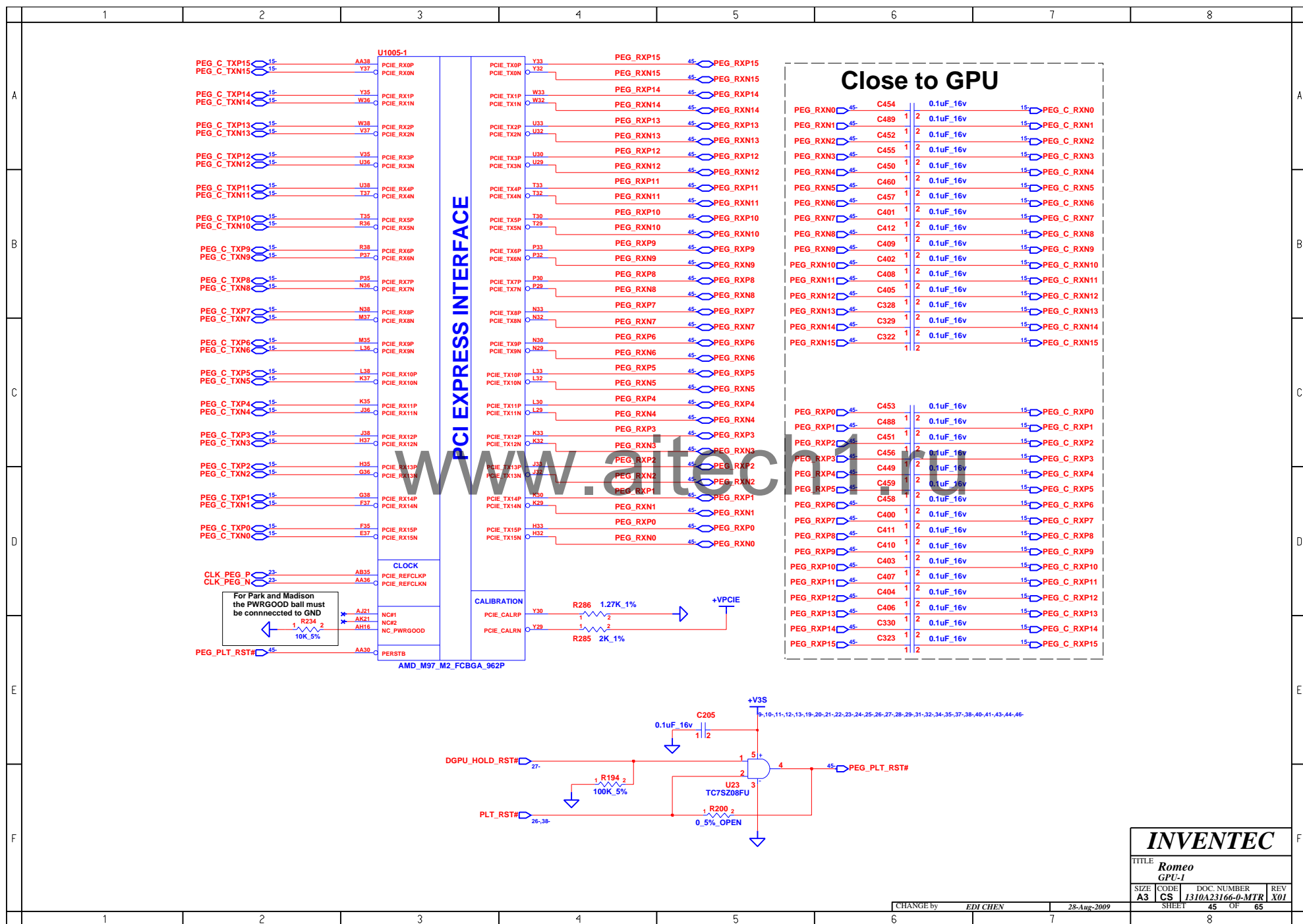


**INVENTEC**

TITLE			
Romeo			
Reserve Page			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		44	OF 65

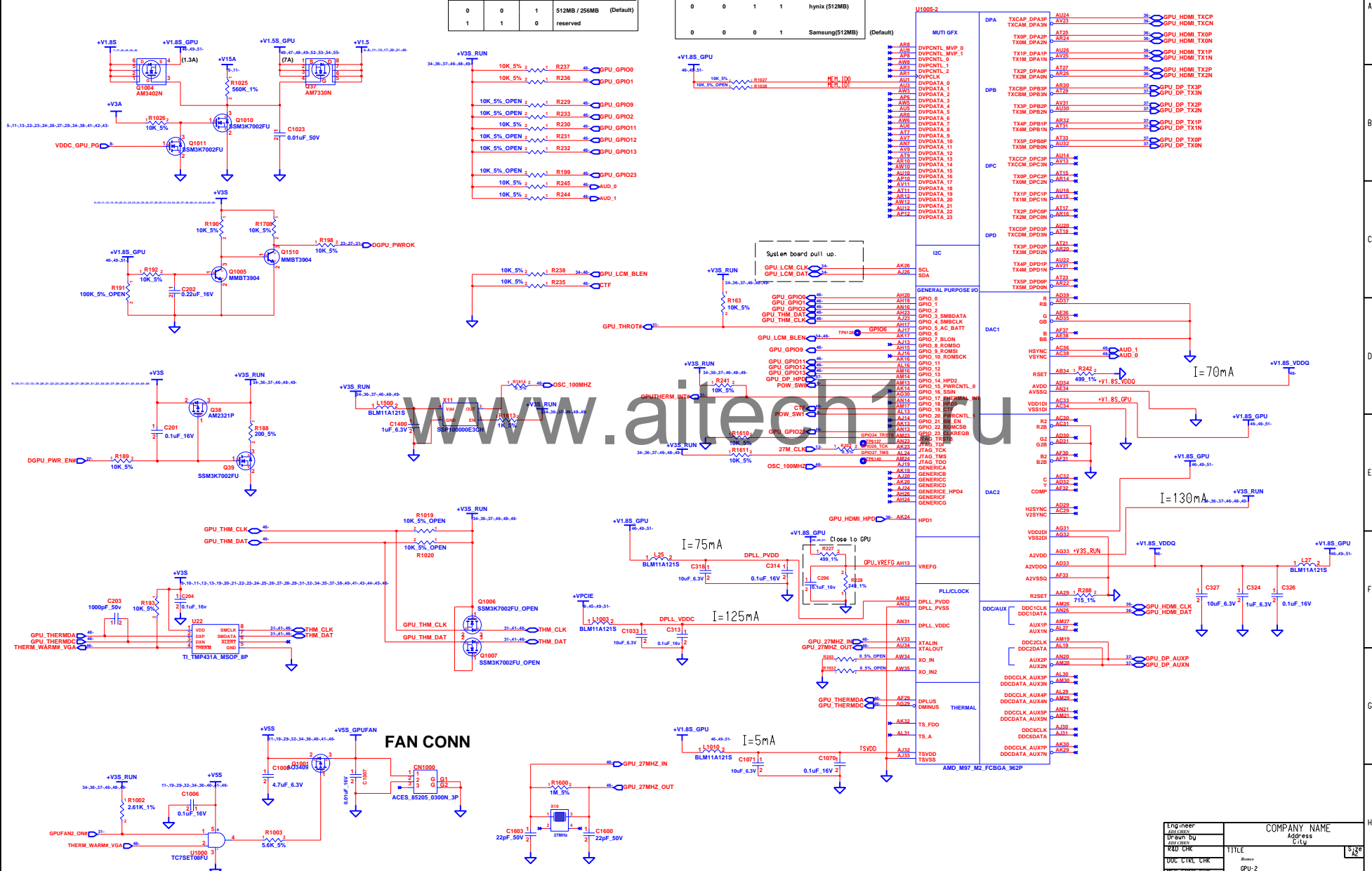
CHANGE by EDI CHEN 28-Aug-2009





If GPIO_22_EN = 0, then GPIO(13:11) defines the primary memory aperture size.			
GPIO_13	GPIO_12	GPIO_11	Memory aperture size
0	0	1	512MB / 256MB (Default)
1	1	0	reserved

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	vendor
0	0	1	1	hynix (512MB)
0	0	0	1	Samsung(512MB) (Default)



Engineer	COMPANY NAME
Drawn by	Address
Check	City
DOC. CTRL. CHK	TITLE
ENG. ENGR. CHK	Rev: GPU-2
Designed by	REV
Date Changed	Drawing Number
1.0	Sheet 46 of 65

**Channel A**

**MEMORY INTERFACE A**

**DDR3/GDDR3 Memory Stuff Option**

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R

**FOR M97,Broadway,Madiso and Park ONLY**

**INVENTEC**

**Romeo GPU.3**

**CHANGE by EDI CHEN 28-Aug-2009**

**SIZE A3 CS 1310A23166-0-MTR REV X01**

**SHEET 47 OF 65**

	<b>GDDR5</b>	<b>GDDR3</b>	<b>DDR3</b>
<b>MVDDQ</b>	1.5V	1.8V/1.5V	1.5V
<b>Ra</b>	40.2R	40.2R	40.2R
<b>Rb</b>	100R	100R	100R

**FOR M97,Broadway,Madiso and Park ONLY**

**INVENTEC**

TITLE			
<b>Romeo</b> <b>GPU-3</b>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b>1310A23166-0-MTR</b>	<b>X01</b>
SHEET		47	OF 65

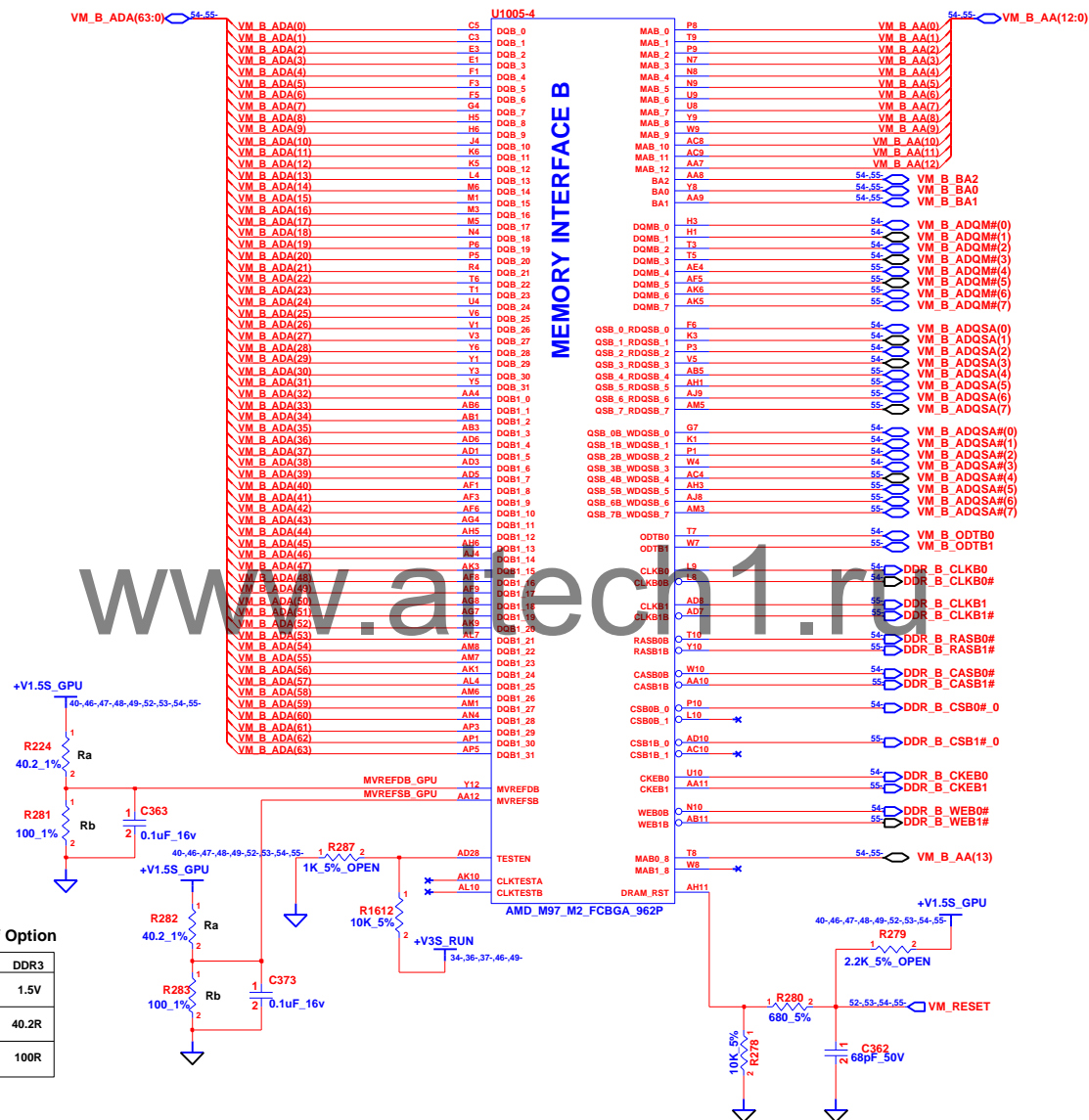
CHANGE by	<i>EDI CHEN</i>	28-Aug-2009
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Channel B

www.aec1.com

DDR3/GDDR3Memory Stuff Option

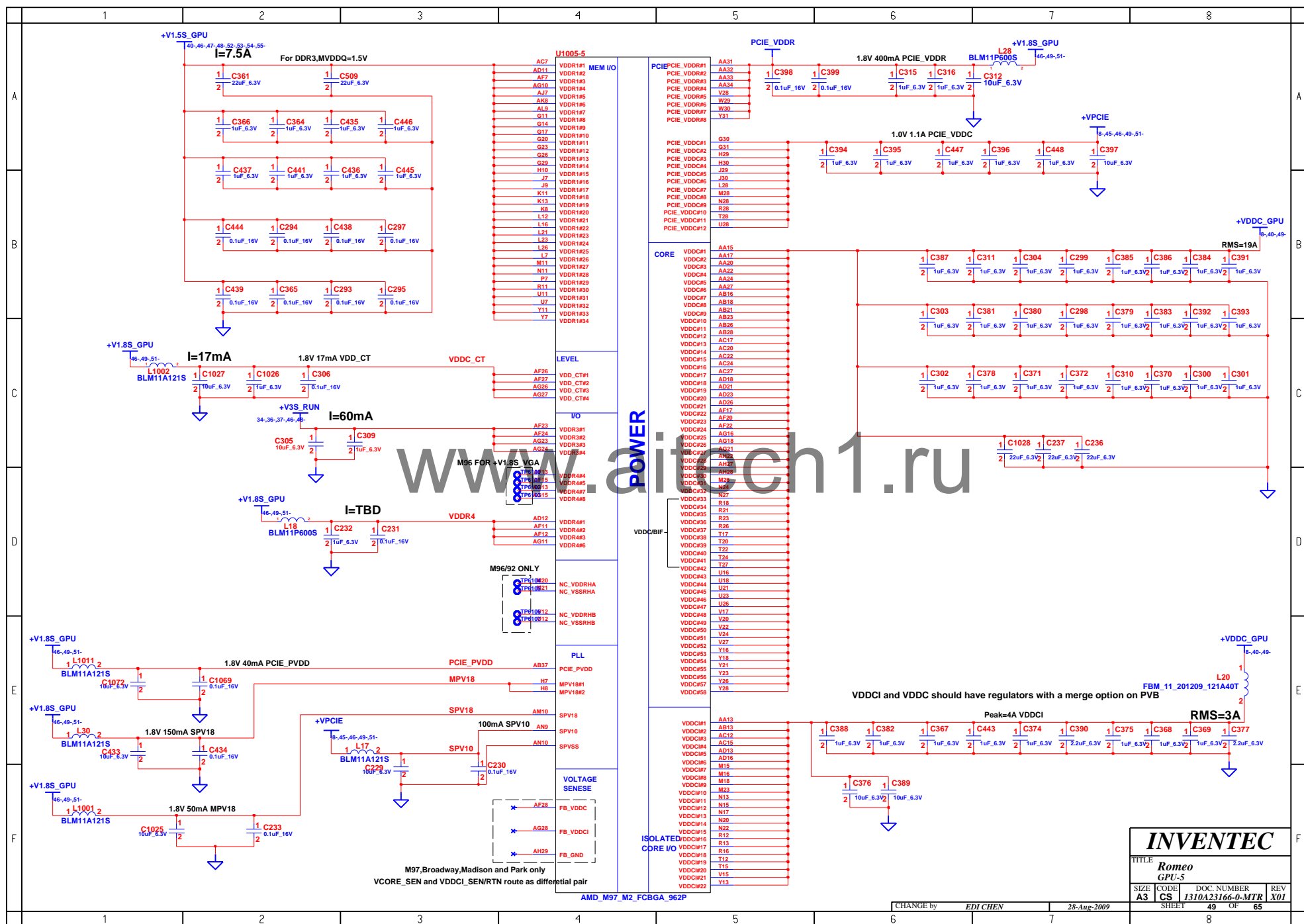
	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R



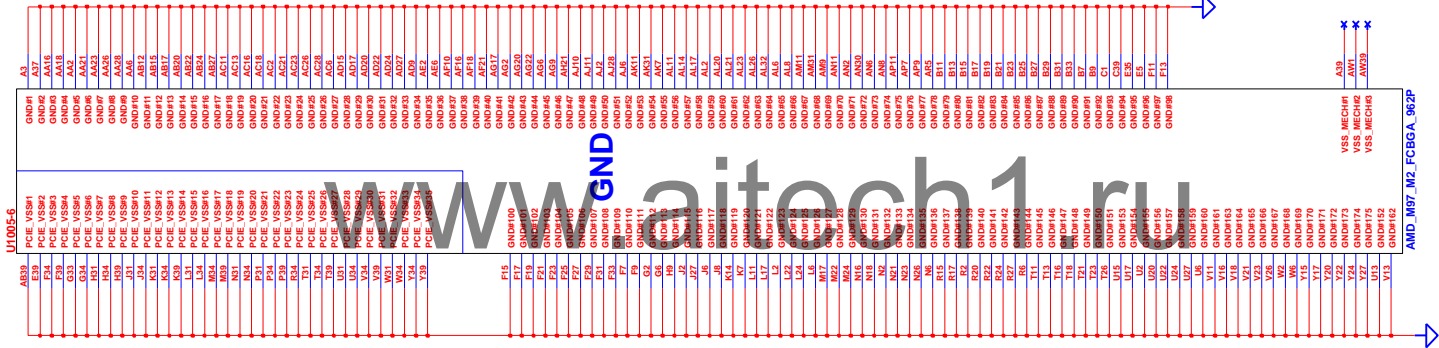
INVENTEC

TITLE			
Romeo GPU-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	X01
SHEET		48	OF 65

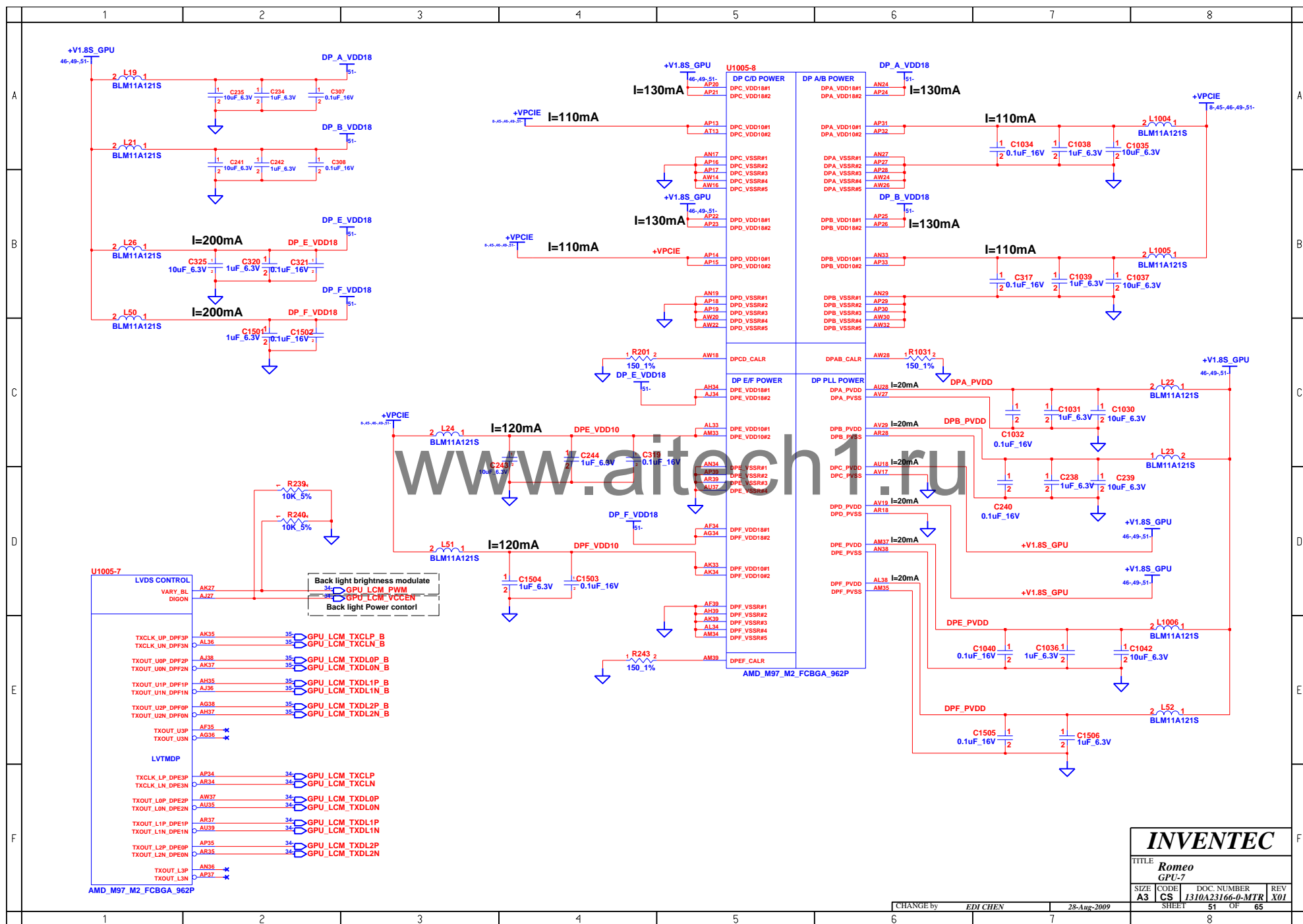
CHANGE by EDI CHEN 28-Aug-2009

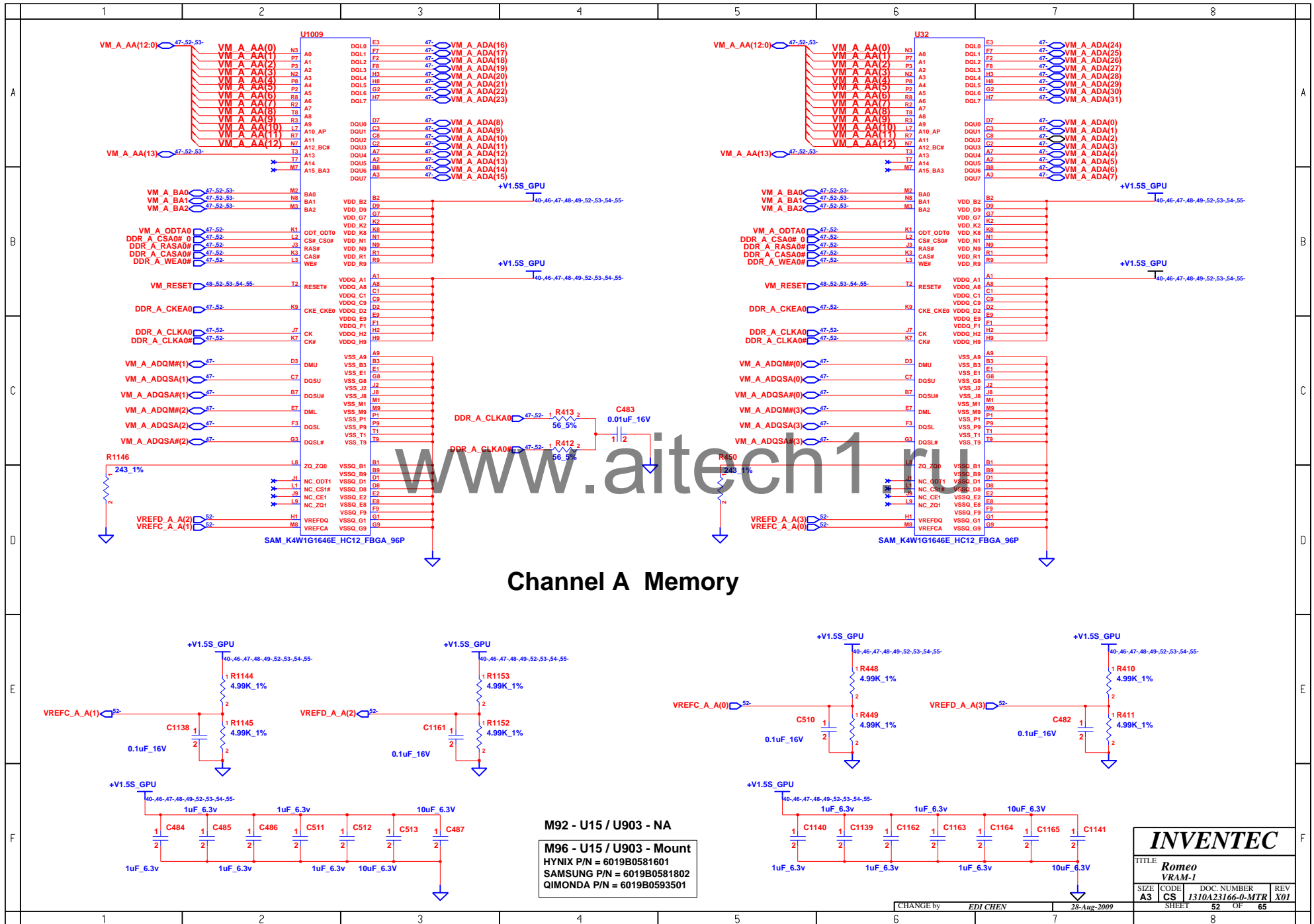


	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	



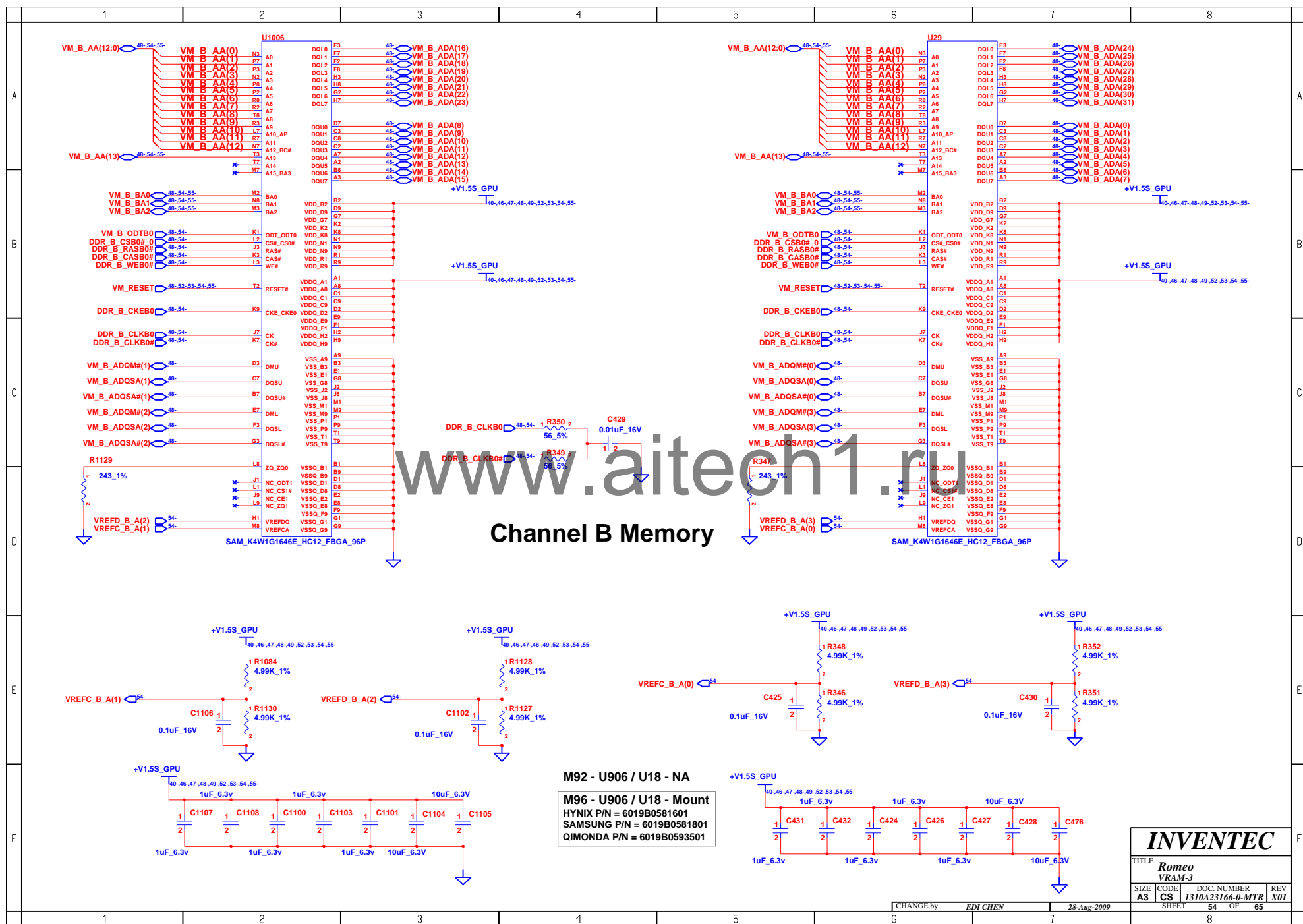
<b>INVENTEC</b>			
TITLE <b>Romeo GPU-6</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>X01</b>
SHEET <b>50</b>		OF <b>65</b>	







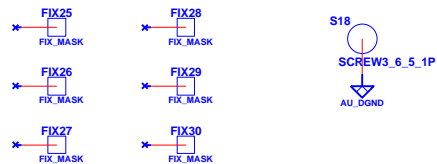
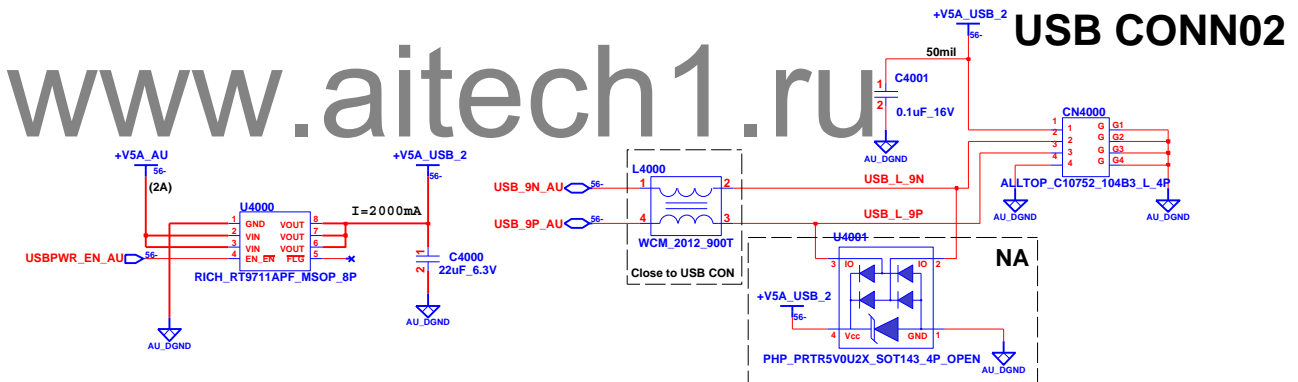
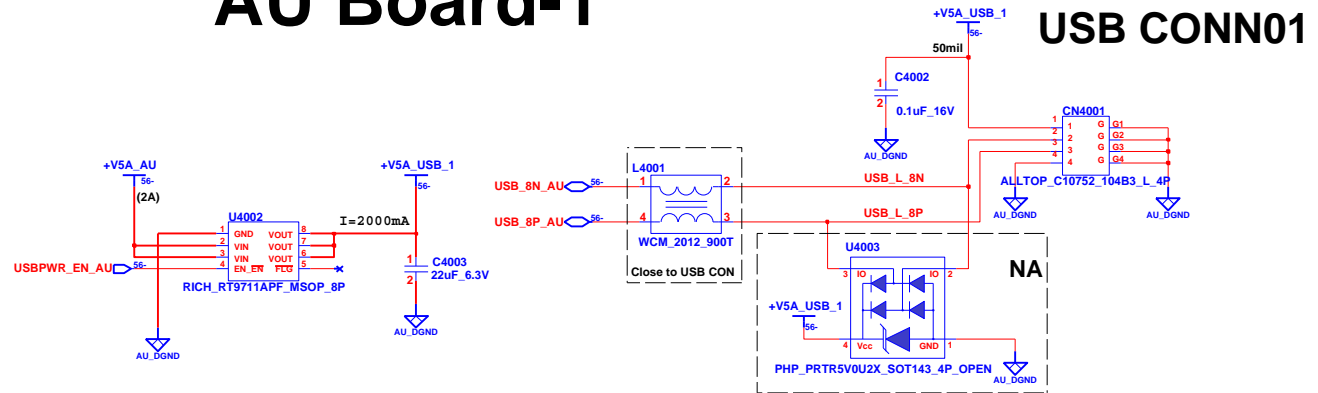
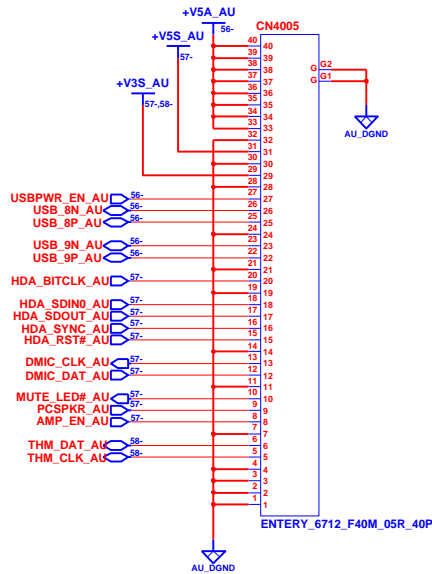






# AU Board-1

## AU Board CN on DB



**INVENTEC**

TITLE  
**ROMEO**

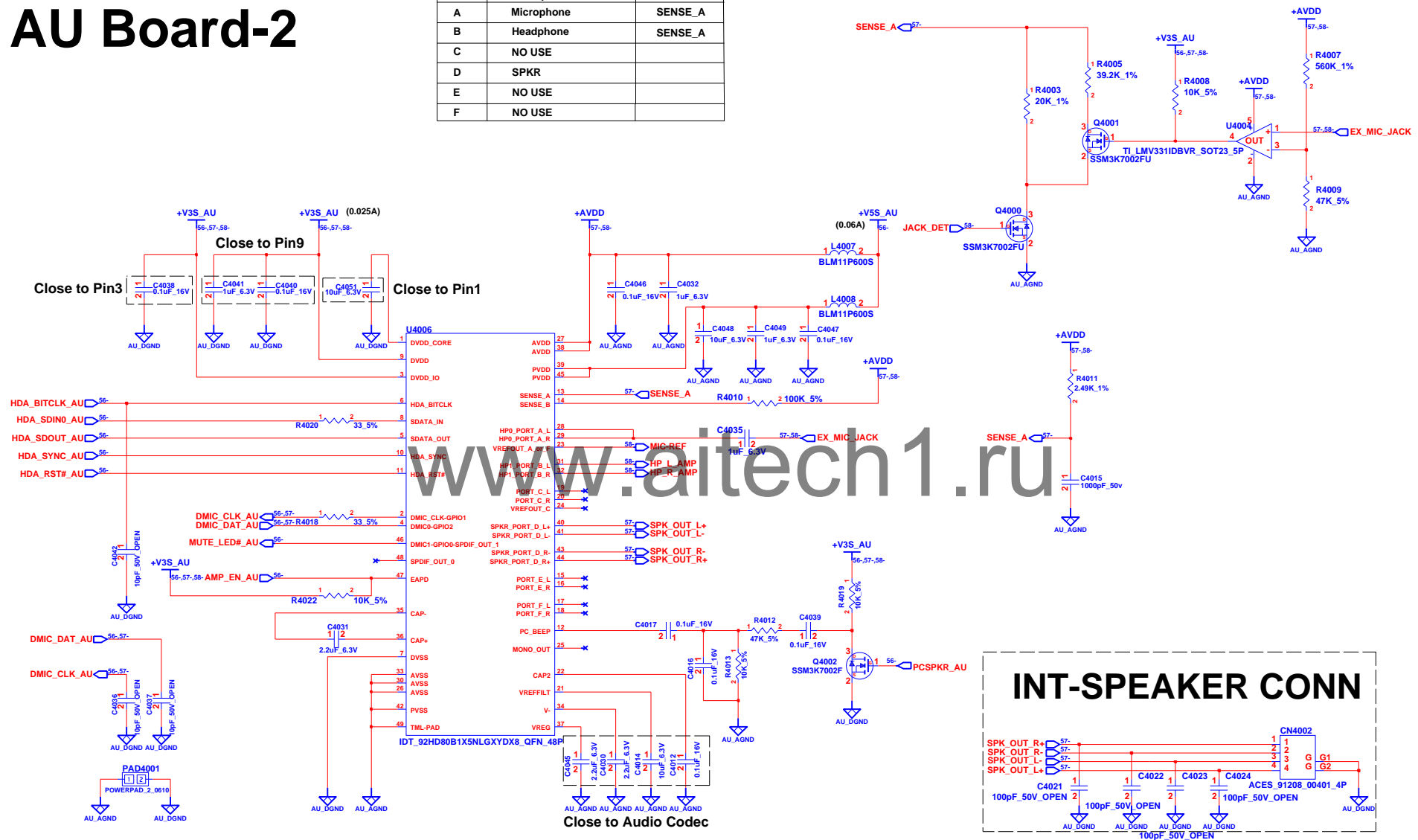
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23165-0-MTR	X01

CHANGE by **EDI CHEN** 20-Aug-2009

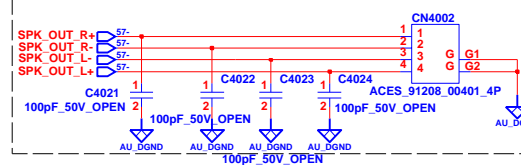
SHEET 56 OF 65

# AU Board-2

PORT	Discription	Sense
A	Microphone	SENSE_A
B	Headphone	SENSE_A
C	NO USE	
D	SPKR	
E	NO USE	
F	NO USE	



## INT-SPEAKER CONN



INVENTEC

TITLE	<i>ROMEO</i>
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SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23165-0-MTR</b>	REV <b>X01</b>
SHEET 57 OF 65			

CHANGE by	<i>EDI CHEN</i>	<i>11-Sep-2009</i>
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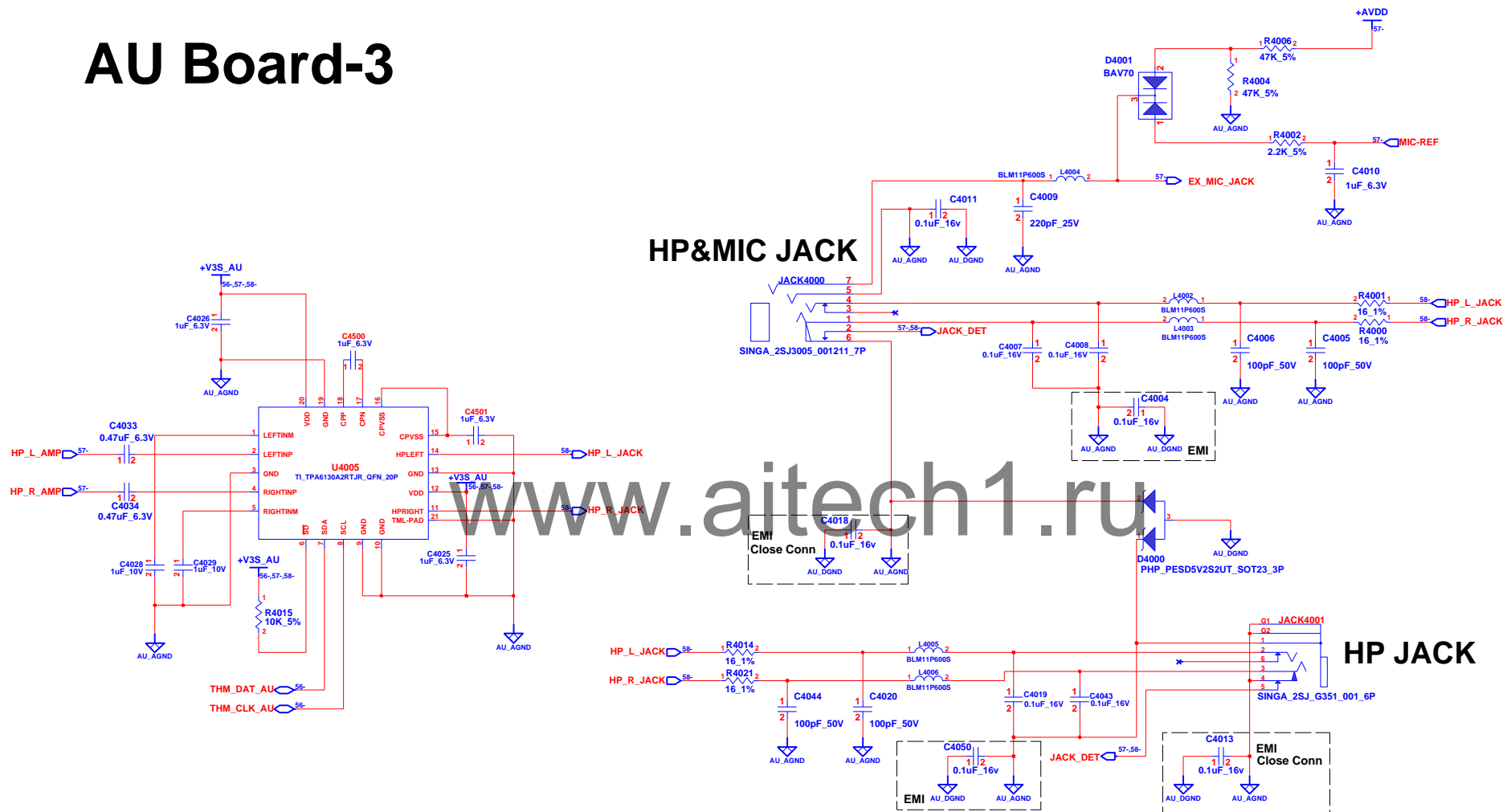
8

# AU Board-3

## HP&MIC JACK

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## HP JACK



**INVENTEC**

TITLE  
**ROMEO**

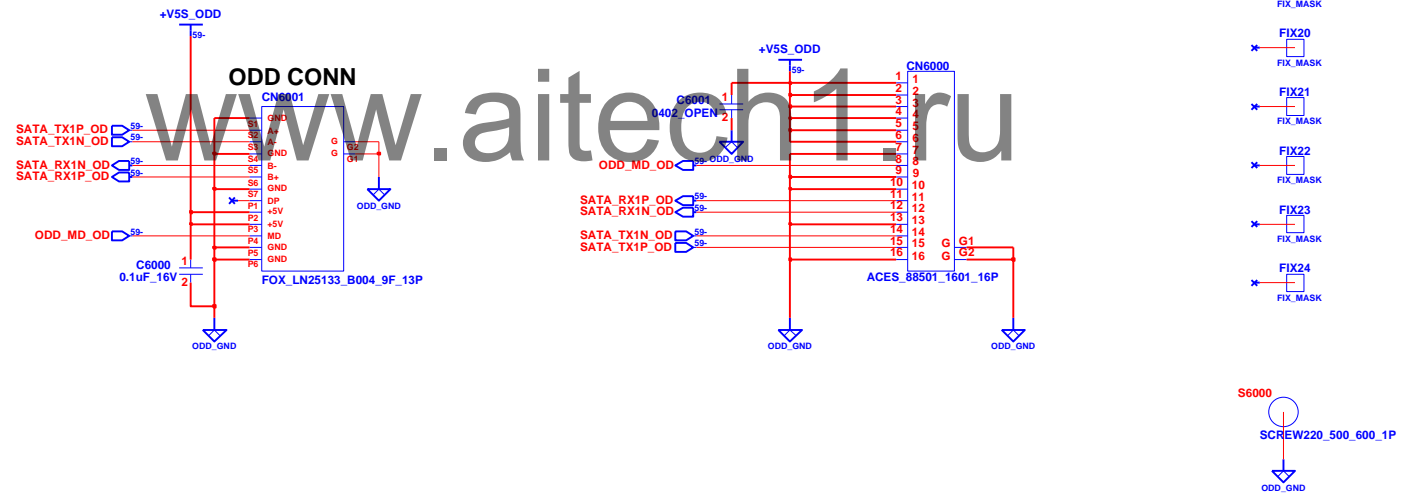
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23165-0-MTR	X01

CHANGE by **EDI CHEN** 20-Aug-2009

SHEET 58 OF 65

# SATA ODD

## ODD Board



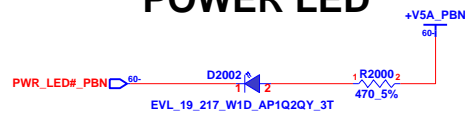
**INVENTEC**

TITLE <b>ROMEO ODD EXT BOARD</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23162-0-MTR</b>	REV <b>X01</b>
SHEET <b>59</b>		OF <b>65</b>	

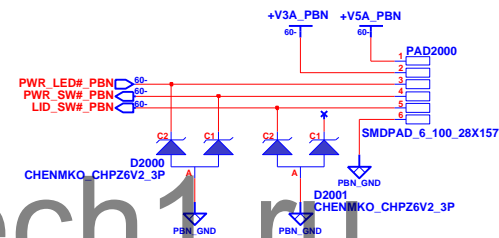
CHANGE by **EDI CHEN** 20-Aug-2009

# Power Button

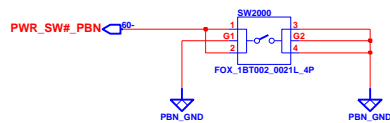
## POWER LED



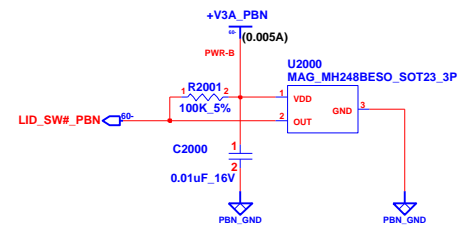
## PBN Cable CN on PBN



## POWER BUTTON



## LID SWITCH



**INVENTEC**

TITLE  
**ROMEO**

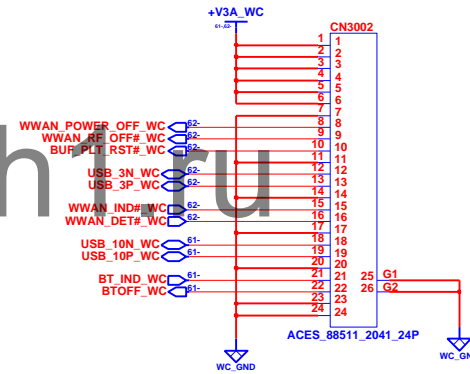
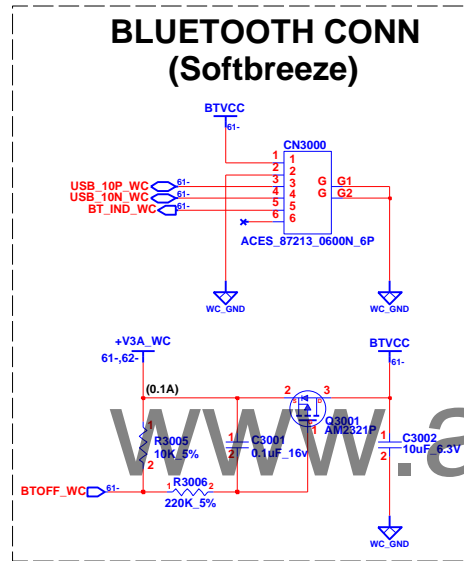
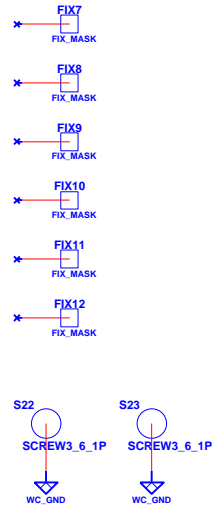
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23174-0-MTR</b>	REV <b>X01</b>
-------------------	-------------------	--	-------------------

CHANGE by **EDI CHEN** 20-Aug-2009

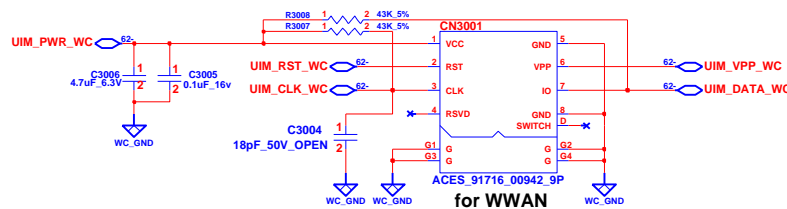
SHEET **60** OF **65**



# WC Board-1



## SIM CARD CONN



**INVENTEC**

TITLE  
**ROMEO**

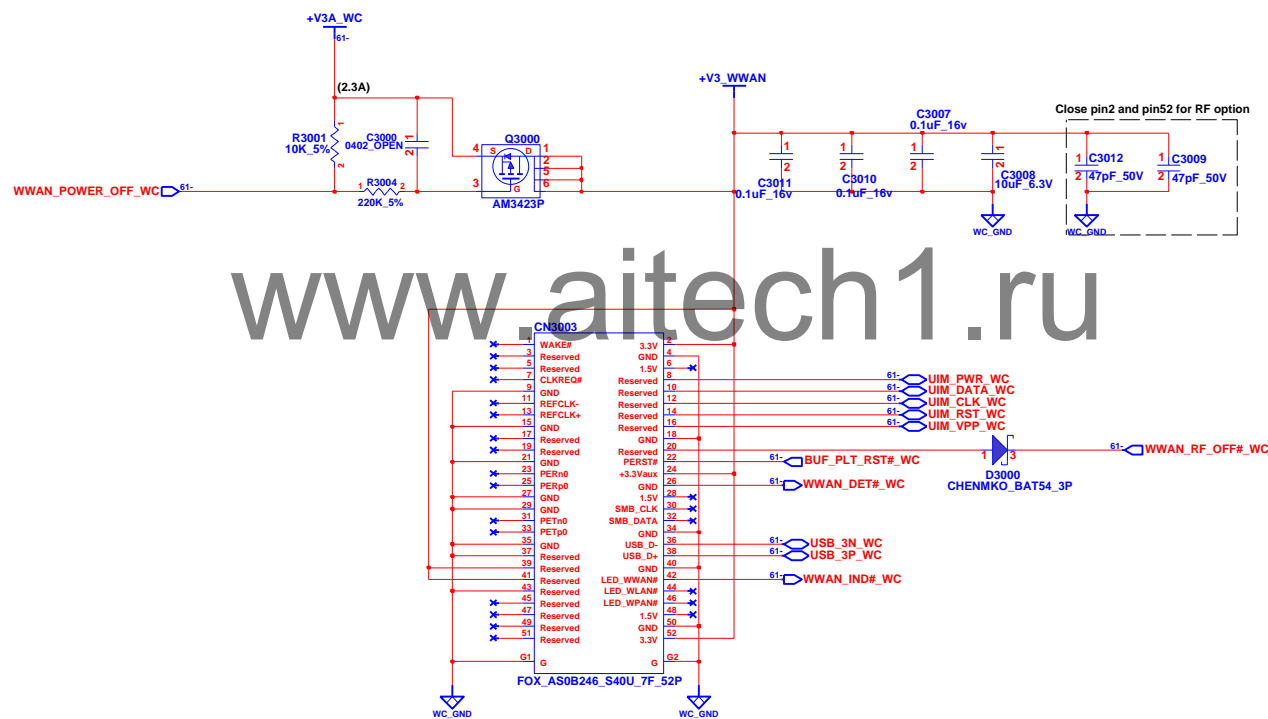
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23164-0-MTR	X01

CHANGE by **EDI CHEN** 20-Aug-2009

SHEET 61 OF 65

# WC Board-2

## WWAN



**INVENTEC**

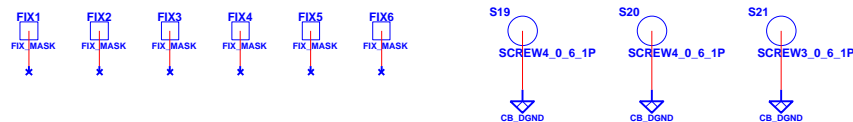
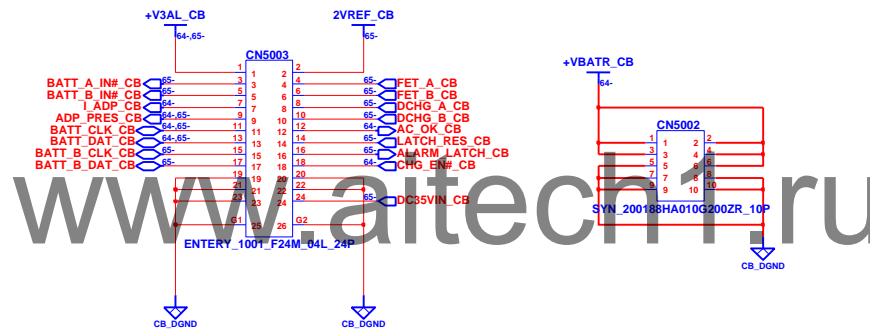
TITLE  
**ROMEO**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23164-0-MTR	X01

CHANGE by **EDI CHEN** 20-Aug-2009

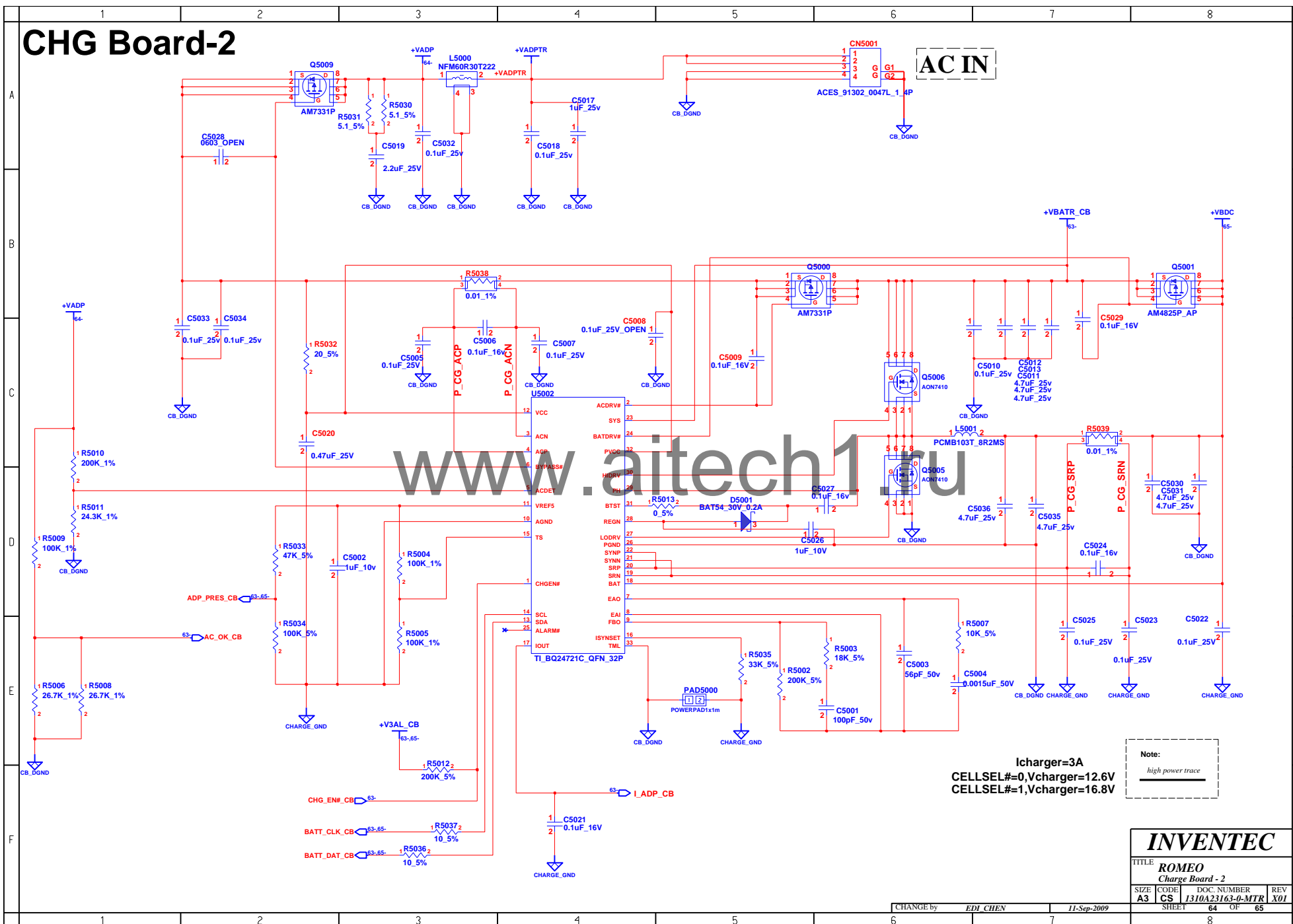
SHEET 62 OF 65

# CHG Board-1



INVENTEC			
TITLE ROMEO Charge Board - 1			
SIZE A3	CODE CS	DOC. NUMBER 1310A23163-0-MTR	REV X01
CHANGE by EDI CHEN		20-Aug-2009	
SHEET 63		OF 65	

# CHG Board-2



Icharger=3A  
CELLSEL#=0,Vcharger=12.6V  
CELLSEL#=1,Vcharger=16.8V

**Note:**  
*high power trace*

**INVENTEC**

TITLE			
<b>ROMEO</b> <i>Charge Board - 2</i>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b>1310A23163-0-MTR</b>	<b>X01</b>
SHEET		64 OF	65

# CHG Board-3

www.aitech1.ru

BATT_A_ID_CB	HI	LOW
SERIES	3	4
VOLTAGE	12.6V	16.8V

BATT_B_ID_CB	HI	LOW
SERIES	3	4
VOLTAGE	12.6V	16.8V

11.3V (12V)  
8.6V (9V)

INVENTEC

TITLE: ROMEO  
Charge Board - 3

SIZE: A3 CODE: CS DOC. NUMBER: I310A23163-0-MTR REV: X01  
SHEET: 65 OF 65

CHANGE by: EDI CHEN 11-Sep-2009

BATT_A_ID_CB	HI	LOW
SERIES	3	4
VOLTAGE	12.6V	16.8V

**INVENTEC**

TITLE	<b>ROMEO</b> <i>Charge Board - 3</i>
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SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23163-0-MTR</b>	REV <b>X</b>
SHEET		65	OF 65

CHANGE by	EDI CHEN	11-Sep-2003
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